VPI Varnishing Technology Effects on Frequency Characteristics of an Air Core Inductor Used in LISN Circuit Application

Khalil Kanzi *, Majid Kanzi *, and Hamidreza Nafissi *

Abstract – The functional characteristic of LISN circuit, which is used for measurements of conductive noise in mains power line, is basically related to frequency characteristics of passive elements like inductors used in the circuit as well as the frequency response of inductors is highly related to the resins used in the varnishing process. The significant problem in determination of an inductor's frequency characteristic is the intrinsic resistance, inductance and parasitic capacitance. In this triplet, the parasitic capacitance is the major limiting factor of inductor's frequency range. This capacitance depends on inductor design parameters and materials filling the spaces of coil like resin and its coherency after curing process. In this paper, two similar inductors were designed and built. The first inductor was not varnished while the second one was varnished with VPI technology. VPI, or Vacuum, Pressure, Impregnation technology is one of the most reliable methods performing good insulating conditions for electrical circuits and windings based on resins. The measured results show that implying varnishing technology does not significantly affect the frequency response. However, due to mechanical solidity aspects and improved environmental protection, it is better to varnish the inductors.

Keywords: High frequency inductor, Parasitic capacitance, EMI filter, VPI, LISN

1. Introduction

The amount of noise injected to the mains network is related to the noise source impedance and the mains network impedance. None of these impedances are constant nor are time-invariant. In power electronic circuits, these fluctuations arise from switches while turning ON or OFF [1]. In the measuring process of conductive noise, LISN circuit is used for minimizing the effect of impedance nonconformities as well as contradistinction of common-mode and differential-mode noises. Figure 1 shows a simple model of LISN circuit schematically.

The amplitude and spectral power density of conductive noise in mains power line can be measured by a spectrum analyzer. However, the input impedance of a spectrum analyzer usually is not matched to mains power lines especially in higher frequencies. As a result, a matching circuit would be necessary for these measurements. LISN circuits which are in different types, mainly act as low-pass filters with passive elements in the path of high frequency signals. LISN actually matches the impedance of mains network to the 50Ω input impedance of the measurement equipment. The specifications of LISN needed for an appropriate measurement process include [2]:

- The ability of transferring power from source to load
- Providing a low impedance path for noise
- Reducing the effect of source impedance in noise measurements.

Fig. 1. The application scheme of LISN circuit.
turns [6]. The parasitic capacitance of inductor which is due to capacitances between loops, between adjacent layers and between coil and the packaging or shield is modeled as a parallel capacitor, $C_{par}$ [6].

![Fig. 2. Frequency model of an inductor.](image)

Evaluating these parameters by physical dimensions, characteristics of materials and their frequency dependence is considered in several papers [3-7]. For instance, the parasitic capacitance has a major effect on the function of the circuit. In fact, up to the upper frequency limit of conductive noise (namely 30MHz), the dielectric constant of most substances does not vary considerably. However, their reactance is affected intensely by frequency. Moreover, the intrinsic resistance is proportional to frequency due to skin and proximity effects. Finally, the inductance is a function of the frequency variations of the permeability of the magnetic circuit [7, 10].

Intrinsic resistance and parasitic capacitance both have harmful impacts on the function of inductors especially in high frequencies [1]. Consequently, the efforts should be concentrated on alleviating their quantity. Two major approaches exist for reducing the parasitic capacitance of an inductor:

The first approach relies on elimination or reduction of inductor's parasitic capacitance using an extra circuit. After construction of the inductor, if the noise is not attenuated adequately by the filter, an extra circuit would be used for this purpose [12, 13]. This method needs an algorithm for estimating the value of parasitic capacitance with an acceptable precision [14]. Afterwards the compensating circuit should be designed and implemented based on the estimated value [13].

The second method is based on appropriate design of high frequency inductors. As a matter of fact, the type and dimensions of core and the number of layers and turns and their arrangement beside the insulation should be implemented in a manner that the minimum parasitic capacitance could be achieved. Many papers have discussed the calculations of inductor parameters based on the dimensions, the kind of implementation and the material characteristics [3-7, 10]. According to the basic circuit theory, if the internal spaces of an inductor are filled with a resin with permittivity constant greater than the air, the stray capacitance will increase and the frequency response of inductor will generally degrade, although using VPI technology is compulsory in many applications due to electrical and mechanical considerations. In this case, if the frequency response of the inductor would be important, the impact of stray capacitance should be considered precisely. The definite instances to be mentioned are EMI filters and LISN circuits applied in electrical propulsion systems. There are no explanations about the insulating quality of the filling resin in high frequency inductors especially in LISN circuits so far.

A 10A single-phase LISN circuit has been designed and constructed. The detailed design was adopted from [8]. The frequency behavior of LISN circuit was investigated by two types of inductors with the same design. The first type was completed with resin by VPI technology and the second was used without varnish. The frequency characteristics of these two inductor types are compared in section 4.

2. VPI Technology

Varnishing of circuits and inductors is common for mechanical robustness and extra environmental protection. Resin is implied as the main insulation substance for isolating different parts in high voltage elements. Varnishing is implemented also widely for fixing the electrical circuits in systems exposed to shocks and mechanical vibrations. VPI technology basically consists of two overall steps. First, injecting resin into electrical circuit in order to fill all gaps, and then, thermal operations for Curing. Both steps are under constraints which at all form the VPI process. Resins, which are produced and presented to market by several producers, represent diverse electrical, thermal and mechanical qualities after Curing process.

2.1 The main elements forming the VPI System

- **Resin Container.** This container reserves resin and should include a mixer. This system mixes the resin and the thinner material before varnishing in order to extend the concentration of resin to a desired level. Moreover, a heater system is required to increase the temperature of resin up to an appropriate level prior to varnishing operation. These systems should be free of any sparks because resin contains flammable gases. The saving tank can have a small opening for adding resin or for entering maintenance personnel into the tank while
it needs cleaning.

- **Varnishing operation tank.** The subject is placed in this tank. The tank has a large opening equipped with an anti-spark system and is connected to a vacuum pump and a Noble gas tank. The resin inlet is normally from the bottom of the tank.

- **Vacuum system.** This system is used for making vacuum in the varnishing tank. The significant specification of vacuum system is the value of its positive pressure, namely in order of 1 mbar. Moreover, its flow determines the speed of making vacuum.

- **Noble gas tank.** This tank contains octet (noble) gas which is injected into the varnishing tank. Octet gases are full gases and anti-combustion and are implemented for squeezing the resin.

- **Thermal furnace.** The resin used in VPI technology reaches to the final quality only under a specific thermal cycle named as Curing Process. Implying this thermal cycle needs a heating furnace. The varnished apparatus enters this furnace just after exiting varnishing tank and is exposed to the specified time-thermal cycle.

![Fig. 3. VPI system schematic](image)

2.2 The specifications of exploited system

- Volume of tanks: up to 2000 liters
- Diameters of tanks: app. 110cm, effective height: 200cm
- Vacuum pump 1: It should have higher flow rate and higher minimal positive pressure. This pump is implemented initially for faster vacuumizing to a positive pressure level normally less than 100 mbar.
- Vacuum pump 2: It should have lower flow rate and lower minimal positive pressure. This pump is implemented in the second stage for slower vacuumizing to a positive pressure level lower than the first stage, namely under 1 mbar.
- 4 capsule with 44 liters capacity all containing CO2 gas.
- Compressor with a 300 liters tank.

This system is depicted in figure 4.

![Fig. 4. The exploited VPI system](image)

**2.3 VPI Process**

- Measuring resin's concentration and correcting it while necessary.
- Resin heating.
- Preheating the subject.
- Placing the subject in the varnishing tank.
- Vacuumizing the varnishing tank.
- Transferring of resin from resin container to varnishing tank up to an appropriate level.
- Implying positive pressure higher than atmosphere pressure using noble gases.
- Transferring the varnished subject to heating furnace for the Curing process.

**2.4 Technical specifications of used resin (CC-1144)**

**2.5 VPI quantities for CC-1144 resin**

- Preheating of subject(coil) up to 105~110°C and putting it in varnishing tank.
- Vacuumizing the varnishing tank to 4 mmHg in 20 minutes.
- During this time, the temperature of coil should not decrease from 50°C.
- Entering resin into varnishing tank gradually so that the level of resin is 2 cm over the coil.
- The pressure of vacuum should be maintained at the specified level for 30 minutes.
- Removing the vacuum and implying a 6–8 Atm pressure for 20 minutes.
- Removing the excess pressure and exiting coil from the varnishing tank then placing it in the heating furnace for Curing.
60 VPI Varnishing Technology Effects on Frequency Characteristics of an Air Core Inductor Used in LISN Circuit Application

<table>
<thead>
<tr>
<th>Table 1. CC-1144 resin technical specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>General</strong></td>
</tr>
<tr>
<td>Application</td>
</tr>
<tr>
<td><strong>Physical properties</strong></td>
</tr>
<tr>
<td>Specific gravity at 25°C</td>
</tr>
<tr>
<td>Viscosity ford cup N.4 at 25°C</td>
</tr>
<tr>
<td>Flash point</td>
</tr>
<tr>
<td>Gel time at 100°C</td>
</tr>
<tr>
<td>Gel time at 110°C</td>
</tr>
<tr>
<td>Thermal life</td>
</tr>
<tr>
<td><strong>Curing properties</strong></td>
</tr>
<tr>
<td>Time at oven hr</td>
</tr>
<tr>
<td><strong>Electrical properties</strong></td>
</tr>
<tr>
<td>Dielectric constant, 1KHz at 25°C</td>
</tr>
<tr>
<td>Volume resistivity at 25°C</td>
</tr>
<tr>
<td>Surface resistivity at 25°C</td>
</tr>
<tr>
<td>Dissipation factor, 1KHz at 25°C</td>
</tr>
</tbody>
</table>

3. Design and Construction of LISN Circuit

An LISN circuit according to CISPR 16-1 with the V-network specification 50 Ω /50μH + 5 Ω is shown in Fig. 5:

![Fig. 5. An LISN circuit; 50 Ω /50 μH + 5 Ω V-network](image)

As it could be seen, there are resistors, capacitors and inductors in the circuit.

3.1 Selecting resistors types

All resistors are of carbon film type because the frequency characteristic of this type of resistors is independent from frequency up to 30 MHz. R3 and R4 are 5W and all others are 1W.

3.2 Selecting capacitors type

There are two types of capacitors used in this circuit: First is metalized polypropylene type. C1 to C4 are from this type. Second is metalized polyester type. C5 to C8 are from this type.

3.3 Selection, design and construction of inductors

- The required equations for design are adopted from previous papers [8] and the following results were implemented for construction:
- The inductor's current density was selected about 3 A/mm². Consequently, for 10A current a 2.10mm wire was used. It was a double coating wire of class H.
- The bobbin was fabricated from a cylindrical threaded fiber material of class H and specified dimensions.
- According to design equations of air core inductors, a 250μH inductor needs 96 turns wounded in three layers, and a 50μH inductor has 50 turns in one layer.
- For a 3-layer inductor, the center-to-center distance of adjacent turns is about 3.6mm. This selection is due to the higher stray capacitance of 3-layer inductor compared to single layer one. As a result, the selected distance was larger in order to reduce this capacitive property.
- The inter-layer space of 1 mm was produced using class H Nomex insulator.
- For a single layer inductor, the selected center-to-center distance between two adjacent turns was about 2.7mm.
- After design, the inductors were built in two methods, namely with varnishing using VPI technology and without varnishing.

Two 250μH inductors produced with this method are depicted in figure 6.

To investigate the difference between these two inductors and the effect of varnishing, the inductors' parameters should be compared via measurement and estimation. This method has been explained in [14]. Measuring the
impedance of inductor with an impedance analyzer in a frequency range covering the bandwidth around resonance frequency and then estimation of inductor's parameters according to frequency variations has been considered.

![Inductor Images](image1.png)

**Fig. 6.** 25μH inductors (a) without varnishing (b) after varnishing with VPI technology

Measurements show that varnishing, although completely coherent, does not affect the inductor's impedance frequency characteristic considerably. Albeit as apparent in figure 7, there exist some undesired effects namely a tiny reduction in inductor's resonance frequency (about 10 KHz), besides, a 4 k decrease in maximum impedance magnitude in resonance frequency.

4. Comparing the Parameters of Two Inductors

In order to compare two inductors and distinguish their difference, parameters $L_{ind}$, $R_{par}$ and $C_{par}$ should be estimated for these two inductors according the model of figure 2 and then compared. Depending on the parametric estimation algorithm discussed in [14], for a 250 μH inductor and a 3 dB bandwidth around the resonance frequency, the parameters have been estimated as follows:

4.1 Parameters of the 250 μH inductor without varnishing

The resonance frequency for this inductor is about 1.004 MHz in which the magnitude of inductor's impedance is 36 kΩ. The estimated parameters of the model of figure 2 in the 3 dB bandwidth from the starting frequency 0.9866 MHz up to the ending frequency 1.0242 MHz have been estimated and illustrated in figures 8 to 12.

The magnitude of the resistance in the estimation frequency range is between 34.3589Ω and 82.9865 Ω.

![Graphs](image2.png)

**Fig. 7.** Impedance variations of a normal inductor and a varnished inductor with frequency(a) in a wide frequency range (b) in a narrow frequency range

![Graph](image3.png)

**Fig. 8.** Estimated resistive characteristic of the inductor without varnishing
The estimated inductance is about 213.6 μH.

The estimated capacitance is about 117.48 pF.

The root mean square error of measured and estimated impedance for the inductor without varnishing is about 0.2104 per cent.

4.2 Parameters of the 250 μH inductor varnished with VPI technology

The resonance frequency for this inductor is about 0.997 MHz in which the magnitude of inductor's impedance is 32 k Ω. The estimated parameters of the model of figure 2 in the 3 dB bandwidth from the starting frequency 0.9769 MHz up to the ending frequency 1.0164 MHz have been estimated and illustrated in figures 13 to 17.

The magnitude of the resistance in the estimation frequency range is between 34.08 Ω and 94.18 Ω.

The estimated inductance is about 201.6 μH.
The estimated capacitance is about 126.03 pF.

4.3 Conclusion of Comparing Inductors’ Parameters

As mentioned before, using resin does not have a major impact on inductor's characteristics especially its high frequency behavior and as the measurements reveal, one can claim that it could have a damaging impact because it reduces the resonance frequency and the impedance magnitude as well. Consequently, resin could be useful for improving the coils' mechanical solidity, enhanced environmental protection and some other features; otherwise it could not affect the overall functionality of the circuit.

5. Conclusion

Inductor elements for a single-phase LISN circuit were designed and constructed in two types namely without varnishing and varnished with VPI technology. The characteristics of pressure, vacuum and temperature were adjusted according to resin's datasheet in order to imply a perfect curing process. Next, the inductors' frequency characteristics were compared. The comparison was performed via measurement of impedance magnitude according to frequency and estimating the inductor's model parameters. The comparison showed that there is no major difference between frequency responses of both types of inductors. It could even be claimed that the parasitic capacitance of varnished inductor is a bit greater. The greater capacitance results in reduction of internal resonant frequency and limiting of desired frequency response of
inductor. This happens due to existence of resin among coil's layers and loops and its insulation coefficient which is greater than air (namely 2.5 times). Consequently, if there is no exigency for using resins owing to mechanical or environmental aspects, it should be avoided in order to manage the extra expenses and time. The inductors were used in an LISN circuit for measuring the conductive noise of a boost converter with PFC whose results will be published in another paper.

References


Khalil Kanzi received the B.S degree from K.N.Toosi University and M.Sc from Tarbiat Modaress University in 1984 and 1992 respectively. He received the Ph.D. degree from K. N. Toosi University in Teheran, 2010. He has worked for the ACECR research Institute (K.N.Toosi University branch) in Teheran in the area of LV/HV high power electrical machines (Design and realization) such as AC/DC Traction motors, AC/DC Counter rotating motors in the power of up to 190kw, Eddy current Loads, power electronic and power systems since 1985. He is now working at the ACECR Institute and his working area is design and realization of conducted EMI filters, LISN circuits, electrical machines and FACTS controllers.

Majid Kanzi received the B.Sc. degree in Electrical Engineering from Islamic Azad University of Tehran and M.Sc. in Power Electronics in Kurdistan University of Iran in 2006 and 2010 respectively. Since 2004, he has been working on research area of power quality in Electrical Power Network and from 2009, on Improving Power Factor and Eliminating the EMI Noise in Switching Power Supplies. He also worked on designing and fabrication of electromechanical devices such as eddy current brake, DC counter rotating motor at the ACECR. His interesting fields are EMC/EMI, power electronic devices and FACTS controllers.

Hamidreza Nafissi received the B.Sc and M.Sc degrees both from K.N.Toosi University in 1988 and 1992 respectively. He has worked for the ACECR Research Institute (K.N.Toosi University branch) in Teheran in the area of telecommunications, solid-state microwave amplifiers, solid-state high-power MW broadcast transmitters (MW, SW and FM), HF high efficiency class-D amplifiers, frequency synthesizers, electronic protection circuits for transmitters, audio processing circuits, a wide variety of switching power supplies, switched mode chargers, dc-dc converters, since 1990. He is now a PhD student at the Electrical and Computer Engineering Department of Waterloo University.