Properties of IZTO Thin Films Deposited on PET Substrates with The SiO₂ Buffer Layer

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ABSTRACT

150-nm-thick In-Zn-Tin-Oxide (IZTO) films were deposited by RF magnetron sputtering after a 10 to 50-nm-thick SiO₂ buffer layer was deposited by plasma enhanced chemical vapor deposition (PECVD) on polyethylene terephthalate (PET) substrates. The electrical, structural, and optical properties of the IZTO/SiO₂/PET films were analyzed with respect to the thickness of the SiO₂ buffer layer. The mechanical properties were outstanding at a SiO₂ thickness of 50 nm, with a resistivity of $1.45 \times 10^{-3} \Omega \cdot \text{cm}$, carrier concentration of $8.84 \times 10^{20} / \text{cm}^3$, hall mobility of 4.88 cm²/Vs, and average IZTO surface roughness of 12.64 nm. Also, the transmittances were higher than 80%, and the structure of the IZTO films were amorphous, regardless of the SiO₂ thickness. These results indicate that these films are suitable for use as a transparent conductive oxide for transparency display devices.

Key words : IZTO, Sputtering, Transparent conductive oxide, SiO₂ buffer layer, Thin film

1. INTRODUCTION

Flexible display technology is becoming the next generation of display technology after flat panel display technology. To manufacture these flexible display devices, flexible plastic substrates can be used, such as polyethylene terephthalate (PET), polycarbonate (PC), polylethylene naphthalate (PEN), polyether sulfone (PES). These plastic substrates are light and can be manufactured on a massive scale, which is one of the reasons these plastic substrates are cheaper than recently used glass substrates. However, these plastic substrates have poor physical properties, such as tensile strength and strain, compared to glass substrates. Additionally, their electrical properties and adhesiveness can deteriorate if they are processed at room temperature or high temperature. Also, plastic substrates absorb humidity and gas, which can deteriorate the device.

New Transparent Conducting Oxide (TCO) materials are being investigated in research to solve the low temperature processing problems of plastic substrates. Presently, due to its high transmittance and conductivity, Indium-Tin-Oxide (ITO) is widely used as a TCO in flat panel display devices such as Organic Light Emitting Diodes (OLEDs), Light Emitting Diodes (LEDs), and Liquid Crystal Displays (LCDs). However, because ITO has poor surface roughness and low work function, and has to be processed at high temperature for fine deposition, there are limits in applying it to flexible display devices.

To solve these problems, amorphous ITO and amorphous In-Zn-Oxide (IZO) can be used. But amorphous ITO has insufficient electrical and optical properties. Amorphous IZO does have acceptable electrical and optical properties, but the process of making sintered IZO targets is complex, so the targets are too expensive to manufacture.

Zinc-doped ITO (IZTO, Indium-Zinc-Tin-Oxide) has been investigated to solve these problems. Fine electrical and optical properties can be expected using room temperature processing with IZTO. Because IZTO has a high work function, high electric conductivity, and high transmittance, and it can be deposited at room temperature, it is eligible for use in flexible display devices. If IZTO is used as a TCO, it needs no annealing or cooling process, because low temperature processing is possible.

However, IZTO cannot prevent the permeation of humidity and gas from plastic substrates. To prevent this, a buffer layer with a high work function and low reactivity with oxygen must be deposited between the TCO and the substrate. This buffer layer acts as a barrier that prevents the permeation of impurities, makes the surface smooth, and improves the adhesiveness and electrical properties.

To apply IZTO as a TCO, electron beam deposition, plasma enhanced chemical vapor deposition (PECVD), and a variety of sputtering methods have been researched, considering working pressure, reactive gas density, working temperature, and TCO thickness. The conditions that produce the best properties, such as optimum surface roughness, electrical conductivity, and optical transmittance,
have already been investigated with different deposition methods. However, the materials deposited as a buffer layer and the changes in device characteristics with different thicknesses have not been thoroughly investigated. Therefore, this study investigated the thickness of the buffer layer that produces the best properties by analyzing the differences in the structural, electrical, and optical properties of the IZTO film. The films were deposited by RF magnetron sputtering after depositing SiO$_2$ as a buffer layer on PET film by PECVD. Various thicknesses in the range of 10 to 50 nm were deposited.

2. Experimental Procedure

Plastic substrates were cleaned with alcohol and de-ionized water for about 2 to 3 minutes. IZTO (In$_2$O$_3$ 90 wt%; ZnO 5 wt%; SnO$_2$ 5 wt%) thin films were deposited on well-cleaned 1-inch by 1-inch sized PET substrates (125-um, SKC, SH40) by RF magnetron sputtering at room temperature. As described in Table 1, the RF magnetron sputtering process conditions were as follows: the base vacuum was $4.5 \times 10^{-6}$ Pa, the active gas (Ar) density was 20 sccm, the working pressure was 3 mTorr, and the working temperature was room temperature. IZTO films were deposited with 150 to 160-nm thickness, 50 W of RF power, and a deposition time of 10 minutes.

Before depositing the IZTO films, SiO$_2$ buffer layers were deposited by PECVD with 10 to 50-nm thickness under the process conditions in Table 2. The crystal structures of the IZTO films deposited on the SiO$_2$ buffer layer were analyzed by X-ray diffraction (XRD, XPERT-PRO) from $2\theta=20^\circ$ to $60^\circ$ at 40 kV and 30 mA. The surface morphology of the IZTO films deposited on the SiO$_2$ buffer layer were analyzed by AFM (Atomic Force Microscopy), the electrical properties were analyzed by Hall measurements (HMS-3000, Ecopia), and the optical transmittance was analyzed in the wavelength range of 300 to 800 nm. Based on the results, the figures of merit for the transparent display devices were calculated.

3. Results and Discussion

Figure 1 presents the X-ray diffraction pattern of the IZTO/SiO$_2$/PET films with various thicknesses of SiO$_2$ buffer layer. Except for the intensive peak at $2\theta = 25.9^\circ$, the pattern generally indicates amorphous structure. The intensive peak at $2\theta = 25.9^\circ$ agrees with the peak of the PET substrates presented by other researchers.

Figure 2 presents the surface morphology from AFM images and the average surface roughness of the IZTO/SiO$_2$/PET films with various thicknesses of the SiO$_2$ buffer layer. The plastic substrate absorbs humidity and gas. In the deposition process, if the humidity and gas permeate into the deposited layer, pin holes are generated, which could lead to porous thin films. In these porous thin films, the next layer would be deposited first into these porous regions, and the surface morphology would consequently be deteriorated. Fig. 2(b) shows a deteriorated surface due to above mentioned plastic problem and the relaxation of stress induced to the substrate. This porous thin film couldn’t be used as a TCO.

The SiO$_2$ thin film acts as a buffer layer that can prevent permeation of humidity and gases from the plastic substrates. With increasing thickness of the SiO$_2$ buffer layer (< 30 nm), due to stress relaxation of the substrates, the IZTO thin film is deposited with some disorder, and the surface roughness is also increased. In the region where the thickness of the SiO$_2$ buffer layer is thicker than 30 nm, the SiO$_2$ buffer layer properly acts as a buffer layer, and the surface roughness is decreased. On this smooth surface, there would be an increased deposition rate for the next deposition process, and this means higher adhesiveness.

Figure 3 presents the X-ray diffraction pattern with various thicknesses of the SiO$_2$ buffer layer.
thicknesses of the SiO$_2$ buffer layer. By increasing the thickness of the SiO$_2$ buffer layer (≤ 40 nm), the resistivity of the IZTO films decreased from 1.95 × 10$^{-3}$ Ω-cm to 1.43 × 10$^{-3}$ Ω-cm due to increased carrier concentration and smoother surface, and the sheet resistance of the IZTO film is at its lowest, 95.3 Ω/sq, with 40 nm of SiO$_2$ buffer layer.

Figure 3(b) presents the carrier concentration of IZTO thin films with different thicknesses of the SiO$_2$ buffer layer. The substitution process between Sn atoms and Zn atoms makes a carrier. This process occurs at a sharp point. With increasing thickness of the SiO$_2$ buffer layer (≤ 40 nm), the sharp points in the IZTO surface are increased, thus carrier concentration increased from 5.91 × 10$^{20}$/cm$^3$ to 9.03 × 10$^{20}$/cm$^3$; the 40-nm-thick buffer layer had the highest carrier concentration. The results were almost the same with the trend of surface roughness.

Figure 3(c) presents the hall mobility of the IZTO/SiO$_2$/PET with different thicknesses of the SiO$_2$ buffer layer. With increasing thickness of the SiO$_2$ buffer layer, the Hall mobility was reduced from 5.41 cm$^2$/V-s to 4.84 cm$^2$/V-s, and there was no prominent variation.

The SiO$_2$ buffer layer carried out its role well, preventing the permeation of humidity and gas. This buffer layer improved the quality of the IZTO film by making the film’s surface smooth, increasing carrier concentration, and reducing resistivity.

Figure 4 presents the optical transmittance of the IZTO/SiO$_2$/PET films after depositing the SiO$_2$ buffer layer at thicknesses of 10 nm to 50 nm on the PET substrates with the IZTO compound semiconductor on top. The transmittance was analyzed at different wavelengths in the range of 300 nm to 800 nm. After depositing the SiO$_2$ buffer layer and the IZTO film, these IZTO/SiO$_2$/PET films exhibited high transmittance, of 80.5% to 81%, making them suitable
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When fabricated with a 40 nm thick SiO$_2$ buffer layer the device exhibited the best electrical properties: carrier concentration was $9.03 \times 10^{22}$/cm$^3$, sheet resistance was 95.3 $\Omega$/sq. And, with a 50 nm thick SiO$_2$ buffer layer the device had slightly decreased properties: carrier concentration was $8.84 \times 10^{22}$/cm$^3$, sheet resistance was 96.7 $\Omega$/sq. Hall mobility was almost the same at all SiO$_2$ buffer layer thicknesses. However, optical transmittance must be considered for transparent devices. Considering the optical transmittance, the figure of merit was $1.20 \times 10^{-3}/\Omega$ for the 40 nm thick SiO$_2$ buffer layer, while the figure of merit was $1.22 \times 10^{-3}/\Omega$ in the case of the 50 nm SiO$_2$ buffer layer, due to its slightly higher optical transmittance. The 50 nm SiO$_2$ buffer layer thickness was demonstrated to be the best case for transparent devices according to the figure of merit.

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### REFERENCES


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### Fig. 4
Transmittance of IZTO/SiO$_2$/PET films with various thicknesses of the SiO$_2$ buffer layer.

### Fig. 5
Average transmittance and figure of merit ($\phi_{TC}$) of IZTO/SiO$_2$/PET films with various thicknesses of the SiO$_2$ buffer layer.

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for use as the TCO in transparent display devices. With increasing SiO$_2$ buffer layer thickness, the graph shape of the transmittance was almost the same. This means the SiO$_2$ buffer layer thickness had only a small effect on the transmittance.

Figure 5 presents the figure of merit ($\phi_{TC}$) and average transmittance in the visible region (400 to 800 nm) of IZTO/SiO$_2$/PET films with different thicknesses of the SiO$_2$ buffer layer. The figure of merit for transparent conductive oxide, $\phi_{TC}$, which was introduced by Haacke, is defined by $\phi_{TC} = T^{10}/R_{\text{sheet}}$, where $T$ is the average transmittance in the visible region, and $R_{\text{sheet}}$ is the sheet resistance analyzed in Fig. 3(a). In the case of the SiO$_2$ buffer layer thickness of 50 nm, the figure of merit, $\phi_{TC}$, had the highest value of $1.22 \times 10^{-3}/\Omega$.

### 4. Conclusion

This paper analyzed the structural, morphological, electrical, and optical properties of IZTO thin films deposited on PET substrates with various thickness of SiO$_2$ buffer layer.