Photoelectrochemical Hydrogen Production on Textured Silicon Photocathode

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(Received October 19, 2011 : Accepted October 24, 2011)

Abstract : Wet chemical etching methods were utilized to conduct Si surface texturing, which could enhance photoelectrochemical hydrogen generation rate. Two different etching methods tested, which were anisotropic metal-catalyzed electroless etching and isotropic etching. The Si nano-texture that was fabricated by the anisotropic etching showed ~25% increase in photocurrent for H2 generation. The photocurrent enhancement was attributed to the reduced reflection loss at the nano-textured Si surface, which provided a layer of intermediate density between water and the Si substrate.

Keywords : Silicon texturing, Metal-catalyzed electroless etching, Photoelectrochemistry, Hydrogen evolution reaction, Solar water splitting

1. Introduction

Recently a renewed interest has been shown in solar water splitting as a potential solution to provide emission-free clean hydrogen fuel for transportation and other energy applications.\(^1\) Photoelectrochemical hydrogen production at the semiconductor/electrolyte interface has been studied for decades.\(^2-4\) In solar fuel production, a photoelectrochemical cell converts photon energy into chemical energy and generates H\(_2\) on cathode and O\(_2\) on anode, as described in the following half-cell reactions:

\[
2\text{H}^+ + 2e^- \rightarrow \text{H}_2 \quad \text{(cathode)} \quad (1a)
\]

\[
\text{H}_2\text{O} \rightarrow \frac{1}{2} \text{O}_2 + 2\text{H}^+ + 2e^- \quad \text{(anode)} \quad (1b)
\]

The Gibbs free energy change \(\Delta G^\circ\) of the net reaction is 237 kJ/mol, or equivalently, the standard emf \(\Delta E^\circ\) is \(-1.23\) V. As the negative emf indicates, the net reaction is energetically uphill reaction.

Silicon is earth-abundant, relatively low-cost semiconductor material that is most widely used in current photovoltaics, with vast knowledge base and infrastructure.\(^5\) The photoelectrochemical H\(_2\) generation at the Si/electrolyte interface has been studied for decades.\(^6-10\) Compared to the solid-state solar conversion devices, one of the disadvantages of the solar energy conversion devices based on semiconductor/electrolyte interface is that it very difficult or impossible to introduce anti-reflection layer (e.g., SiO\(_2\) and/or Si\(_x\)N\(_y\) coating on top of the p-n junction in the conventional Si solar cells), because such layer will easily block the electron transfer at the semiconductor/electrolyte interface. As depicted in Fig. 1, due to the large difference in the refractive indices of Si (\(n = 3.5\)) and electrolyte (\(n = 1.3\)), the planar Si/electrolyte interface reflects as much as 25% of the incident visible light.\(^11\) Alternative approach to reduce

Fig. 1. (A) Schematic showing reflection loss at the semiconductor/electrolyte interface which has a large difference in refractive indexes values (n). (B) Surface texturing can reduce reflection at the semiconductor/electrolyte interface by providing an anti-reflection layer with intermediate density between water and the Si substrate.

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the reflection loss is Si surface texturing. Surface texturing usually refers to introducing micrometer or even nanometer scale embossing structures on the Si surface, which effectively reduces reflection loss of incident photon thus enhances generated photocurrent.\(^{12-14}\) Fabrication methods for the Si surface texturing includes alkaline etch, acid etch, and reactive ion etch (RIE). The RIE method requires expensive and cumbersome ultra-high vacuum equipments. On the other hand, wet chemical processes provide a simple, rapid, and effective route to low-reflectance Si surface texturing. Among several different wet etch systems, the HF/HNO\(_3\) system is the most commonly used low-temperature process. In spite of the direct benefit that can be obtained from the surface texturing, adoption of the surface texturing into the semiconductor/electrolyte interface has been rarely investigated.\(^{11}\)

Here in this work, we utilized two different room-temperature wet etch methods for Si surface texturing: the HF/HNO\(_3\) system and the metal-catalyzed HF etching. Optical and electron microscopy measurements showed that the wet etch methods effectively produced micrometer- or nanometer-sized pits/pores or grooves on the Si surface. The textured Si surface was evaluated by measuring the photoelectrochemical H\(_2\) generation. Effect of the surface texturing on the magnitude of photocurrent was examined and discussed.

2. Experimental

2.1. Wet chemical texturing of crystalline Si

A p-type Si (100) wafer (Wafer World, Inc.; \(\sim\)10 ohm-cm; B-doped) was degreased by rinsing with acetone, isopropyl alcohol, and ultrapure deionized water (DIW) in sequence. The Si surface was further cleaned by immersing in piranha (1 : 3 H\(_2\)O\(_2\) + H\(_2\)SO\(_4\)) for 3 min and in 1 : 10 HF for 2 min to remove surface oxide. Backside ohmic contact was formed by depositing and annealing an ohmic metal layer onto the HF-treated Si wafer. For metal-catalyzed electroless etching, the Si sample was immersed in 8 mM AgNO\(_3\) and 4.6 M HF for 6 min. In the MCEE method, oxidizing agent (Ag\(^+\) in current work) provides holes that are needed to oxidize Si atoms and, at the same time, HF etches away Si atoms.\(^{15-20}\) Etchant composition of the MCEE method

2.2. Photoelectrochemical H\(_2\) generation measurements

For photoelectrochemical measurements, a O-ring sealed Teflon cell was used that could accommodate a three-electrode configuration and secure a light path to the Si/electrolyte interface. Backside ohmic metal was electrically connected to a Cu metal foil. The front side of the Si sample was firmly pressed and sealed with a Teflon O-ring, by which the projected electrode area was defined. (0.286 cm\(^2\)) A Pt counter electrode and a standard calomel reference electrode (SCE) were used. For illumination source, a halogen lamp was used, the intensity of which was controlled by the distance between the lamp and the photoelectrochemical cell. A Si photodiode (Hamamatus; model S2386-18K) was used for intensity calibration. All glassware was thoroughly cleaned in an oxidizing bath (Nochromix, Godax Laboratories, Inc.) and rinsed with ultrapure water prior to use. Electrolyte solution was prepared by adding concentrated H\(_2\)SO\(_4\) (Aldrich) into 0.5 M K\(_2\)SO\(_4\) along with pH measurement. All chemicals were used as received. All measurements were conducted in three-electrode configuration using a CHI 660D potentiostat.

3. Results and Discussion

Fig. 2 shows SEM micrographs of the textured Si, which was fabricated by metal-catalyzed electroless etching (MCEE) of bulk crystalline p-type (100)-Si (10 W-cm; B-doped) in 8 mM AgNO\(_3\) and 4.6 M HF for 6 min. In the MCEE method, oxidizing agent (Ag\(^+\) in current work) provides holes that are needed to oxidize Si atoms and, at the same time, HF etches away Si atoms.\(^{15-20}\) Etchant composition of the MCEE method

![Fig. 2. Scanning electron micrographs of the surface nano-textured Si, which was fabricated by metal-catalyzed electroless etching (MCEE) of bulk crystalline p-type (100)-Si (10 W-cm; B-doped) in 8 mM AgNO\(_3\) and 4.6 M HF for 6 min. (A) Top view of the nano-textured Si surface showing. (B) Profile view of the nano-textured Si layer.](image-url)
is similar to the traditional Si etchants in the sense that both oxidizing agent and HF are present. However, Ag$^{+}$ is reduced to form nanostructured deposit on the Si surface, which functions as nanostructured template in the further etching of the underlying Si. As shown in Fig. 2(A), the Si surface textured through the MCEE method exhibits nano-texture that ranges from 50 to 200 nm in lateral dimension. Cross-sectional image in Fig. 2(B) shows that the vertical dimension of the nano-texture is ~200 nm. The vertical dimension is increased in proportion to etch time. Because the dimension of the nano-texture is comparable to the wavelength of visible light, the nano-textured surface effectively suppresses light reflection at the surface, which is evidenced by ‘black’ surface color with low reflection as compared to mirror-like surface of polished planar Si.

Fig. 3 shows an optical microscope image of the Si surface, which was textured by isotropic etching of the same crystalline Si in a 1 : 1 (v/v) mixture of 60% HNO$_3$ and 50% HF for 10 sec.

The HNO$_3$/HF etch system is the most commonly used isotropic Si etchant composition. Because the HNO$_3$/HF etch system is an isotropic etchant, the image in Fig. 3 exhibits macroscopic grooves tens of microns in lateral dimension, in contrast to the nano-textured features produced by the MCEE method in Fig. 2.

Fig. 4 shows photoelectrochemical measurements of the Si photocathodes, which were textured by the MCEE method, in acidic solution of H$_2$SO$_4$ + 0.5 M K$_2$SO$_4$ (pH 1). During the measurements, the textured Si photocathode was constantly illuminated with simulated sunlight (intensity = 16 mW/cm$^2$). When the Si photocathode absorbs a photon, a minority carrier (electron in case of p-type Si) is generated and flows toward the semiconductor/electrolyte interface, where the minority carrier is transferred across the semiconductor/electrolyte interface, reducing H$^+$ to H$_2$. Fig. 4(A) shows the current density ($J$)-electrode potential ($E$) measurements on the textured Si photocathode. When $E$ is more positive of 0.2 V, the value of $J$ is virtually zero and H$_2$ is not produced. As $E$ is scanned in negative direction, the current increases and, at the potential regime more negative of ~0.2 V, reaches a limiting photocurrent that is determined mainly by the transmitted photon intensity. The sigmoidal $J$-$E$ graph is typical of photon-limited photoelectrochemical response. In Fig. 4(A), the MCEE-textured Si photocathodes exhibits enhanced photocurrent compared to the planar Si. This result indicates that,
while substantial amount of incident photon is reflected at the mirror-like planar Si interface, the textured Si surface significantly reduces reflection loss and generates larger current. Fig. 4(B) shows the effect of etch time on the limiting photocurrent density. Compared to the planar Si (etch time = 0 min), the 6-min electroless etched Si photocathode shows as large as 24% increase in photocurrent. Previous report indicates that, at the mirror-like planar Si/electrolyte interface, as large as 25% of the incident solar radiation is reflected. On the other hand, the nano-textured Si surface provides an anti-reflection layer with intermediate density and refractive index between water and the Si substrate, dramatically reducing reflection at the Si interface.

Fig. 5 shows photoelectrochemical measurements of the Si photocathodes, which were textured by the HNO$_3$/HF isotropic etchant, in acidic solution of H$_2$SO$_4$+0.5 M K$_2$SO$_4$ (pH 1) illuminated with simulated sunlight (intensity = 16 mW/cm$^2$). Comparison of limiting photocurrents indicates that the isotropic etching of Si provides little enhancement. This result indicates that the macroscopic grooving produced from the isotropic etching, as observed in Fig. 3, is not very effective in reducing light reflection from the Si surface. Furthermore, after the isotropic etching, the photovoltage is significantly reduced, indicating formation of passivation layer or change of surface chemistry at the etched Si surface.

4. Conclusion

Two different wet chemical etching methods were evaluate for Si surface texturing to reduce reflection loss at the Si/electrolyte interface: the anisotropic etching assisted by metal ion and the isotropic etching in the HNO$_3$/HF system. Optical and electron microscopy measurements verifies that the MCEE method produces a nano-textured surface that functions as effective anti-reflection layer by providing intermediate density layer between water and the Si substrate, while the isotropic etching produces macroscopic grooves on the Si surface. Photoelectrochemical H$_2$ generation measurements showed that the anisotropic etched Si photocathode exhibited ~25% enhancement in photocurrent, which was attributed to reduced light reflection at the nano-textured Si interface.

References