Embedded-based Power Monitoring Security Module Design

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Abstract

The demonstration project of the electrical grid for Smart grid is progressed, the smart digital appliances AV technology, Smart home energy management technology charging the management function of complex energy for the automation management of air conditioning and heating, humidity and air, the health care technology charging the design of housing for the elderly and disabled and the measurement of individual bio information, and the Smart home security technology dealing with the biometric security and motion sensors, etc. have been studied. The power monitoring terminal which uses a variety of wired and wireless networks and protocol is the target additionally to be considered in addition to the security vulnerabilities that was occurred in the existing terminal. In this research paper, the author analyzes the cryptographic techniques corresponding to the smart meter occurred by the problems that are exposed on the outside which are vulnerable to physical attacks, and intends to propose the design of the security systems for the Smart meter terminal being able to maximize the efficiency of the terminal.

Keywords
Embedded System, Smart Grid, Security Module, Cryptography

I. Introduction

Smart grid is the next generation intelligent electrical grid which IT technology is fused into the electrical grid, and the cyber security threat of this grid is greatly increased in comparison with the existing electrical grid, and the security efforts of Smart grid is constantly being researched. The
development of terminal and various contents for the resulting energy savings and energy reduction is being achieved. Especially, if the device which sensors are built-in it can be directly connected to an external network, measured data is delivered directly to the service provider[1],[2],[3]. On the other hand, if LAN is equipped to appliances, the measured information through the gateway is sent to the external network and then is delivered to the service provider. For example, in the case of monitoring using the environment sensors, the measured data is passed to the gateway in the home using a short-range communication such as ZigBee, Bluetooth, and is sent to external network. At this time, the gateway should be prepared for the security vulnerabilities in addition to the ability to simply transfer data[4],[5],[6].

In this research paper, the hardware device by analyzing ongoing ISO/IEC JTC1 SC25 protocol as international standards for the protocol and interconnection technology associated with electronic equipment and systems in the home related to data interchange/operation through FPGA platform, was designed, and also the hardware applying an elliptic curve cryptographic algorithm corresponding to simple power attack method that is one of the sub-channel the public key–based password in order to obtain the authentication and service for packet data of electronic devices was designed.

II. Power Monitoring Security Module

IEEE PI363, ANSI X9.62(elliptic curve electronic signature algorithm), ANSI X 9.63(elliptic curve key agreement and transport protocol), ISO/IEC(1488 additional type electronic signature–part 3 E1GAMAL type signature algorithm in the certificate–based mechanism), ATM Forum(Proposed as a system that provides confidentiality, authentication, data integrity and access control), PKCS 313(during standard working by Elliptic curve cryptography standard), SEC 2(Elliptic curve cryptography come up as a standard), SEC 2(Elliptic curve cryptography comes up as a standard), etc. that are the standard of the elliptic curve used to the hardware implementation, can be seen[7],[8],[9].

Because the elliptic curve theory used in this paper has a natural group operation and an efficient algorithm that perform the operations as one field of the algebraic geometry that have been studied over the past 100 years, cryptographic applications is easy. Elliptic Curve Crypto system is the crypto system that the multiplication group of the finite field that use in discrete logarithm, is replaced to the elliptic curve group. Efficiency of elliptic curve cryptography system can be seen from the following three viewpoints. Figure 1 shows the entire block diagram of elliptic curve algorithm that is composed of several circuit.

First, the computational complexity side. Where the computation is required in order to perform a public key and a private key transport, and the elliptic curve crypto system has very small computation compared to the existing public-key cryptography.
Second, the size side of key. The elliptic curve cryptography system is very small size of the key, so the less space is required to store arbitrary key.

Third, aspects of the communication bandwidth. In the elliptic curve cryptography system, the communication bandwidth required to encrypt a message or to send signatures is very small.

- The design of Loop structure for the efficiency of optimal resource
- Designing the shortening method of multiplication time improving by transforming Coron’s algorithm as an inversion device and a multiplication device to minimize the performance degradation by SPA correspondence like Figure, 2.

- Appropriate algorithm design in VHDL for FPGA implementation
- FPGA implementation of elliptic curve algorithm and producing of test board for measurement of simple power consumption
- Commands, Internal operations, internal I/O module design and FPGA Implementation
- Memory control design for key management
- External interface design

Figure 3 is the experimental setup diagram measuring the amount of electric power consumed while the security module performs cryptological operation.

![Fig. 3 Experimental equipment diagram of power analysis](image)

Fig. 4 Comparison of power consumption according to operation

Figure 4 shows that the amount of power being consumed by internal basic operations used in cryptographic algorithm through the experiment device such as shown in Figure 3 is different.

### III. Cryptographic Module Interface Design

The need for security one-step enhanced that allow to be provided through only valid devices by
adding the authentication function of information appliances has been raised, and to provide data encryption and integrity to prevent the leakage of individual sensitive information by malicious hackers, and unauthorized use of information appliances, or to prevent the forgery of data became necessary. In this paper, the author intends to provide the confidentiality and integrity of data occurring from information appliances as a hardware module to protect the data of information appliances in the smart home. As the devices armed with ‘smart’ such as smartphone and smart TV have been continually come pouring out, it is the situation that the observation that life appliances rushed to the era of smart appliances is coming out.

Figure 5 shows that N screen service interlocked so that existing analog household appliances can be shared and appreciated the contents between smartphone and smart TV, PC beyond dimension to be replaced by simply digital smart appliances is widely spread, and while being reached the time that the household smart appliances are remote controlled by a smartphone, that the design of embedded-based cryptographic module interface coupled and interoperable with the home network system remotely controlling the use and metering of electricity, gas and tap water, and crime prevention and disaster prevention are proposed.

Figure 6 shows the main board which can monitor and control the amount of power between household appliances and meter to confirm the power transmission of the appliances to the smart grid meter. Figure 6 shows that the communication of transmitting and receiving of terminals (transmission module of household appliances) was designed by applying Bluetooth (Stollmann) 2.0.

Figure 7 shows that the communication of transmitting and receiving of terminals (transmission module of household appliances) was designed by applying Bluetooth (Stollmann) 2.0. Figure 6 is the photograph showing integrated monitoring experiment by collecting the amount of power of the household appliances, and this was designed based on Android, and the wireless transmission of the appliances was designed with Stollman Bluetooth 2.0.
IV. Conclusions

In this paper, the embedded-based electric power monitoring security module which could lead safe energization of Smart home because it is easy to apply to smart appliances due to the authentication or key management and data protection between smart household appliances, and low-area and low-power, and the application of the algorithm corresponds to SPA was designed, and in order to experiment it, Android-based embedded boards have been designed and implemented. Smart grid, as well as u–Health transferring personal biometric information, etc. will be applicable.

In future research directions, the gateways suited for each terminal will be integrated into a gateway for U–healthcare, smart grid and environment sensors and home network configuration, and the author intends to study the security algorithms that can interlock between the heterogeneous devices which the security enhancement function is added, and the integrated system which controls it.

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REFERENCES

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