Progress in R&D of coated conductor in M-PACC project

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Abstract

The five-year national project in Japan for R&D of coated conductors and applications, named as the Materials and Power Applications of Coated Conductors (M-PACC) project, was finished at the end of FY2013. The project consists of four sub-themes as cable, transformer, SMES and coated conductors. In the theme of coated conductors, the fabrication process had been developed to satisfy the requirements from the applications such as in-field $I_e$ performance, low AC loss in the long tapes etc. Through the project, the remarkable progress was achieved as follows; a high in-field minimum $I_e$ value over 54A/cm-width under 3T at 77K was realized in a 200m long EuB2Co tape with artificial pinning centers of BaHfO$3$ by the pulsed laser deposition (PLD) technique on the IBAD template. On the other hand, the AC loss reduction was confirmed in the tapes fabricated by both PLD and the metal organic deposition (MOD) techniques by scribing 100m tapes into 10-filaments. Additionally, the mechanism of the delamination phenomenon was systematically investigated and the strength was improved by eliminating the origins of the weak points in the films. Through the development, all targeted goals were accomplished and the several results were appreciated as a world champion data.

Keywords: YBCO, REBCO, coated conductor, $I_e$, in-field $I_e$, AC loss, mechanical strength, $J_{ce}$ cost, delamination, APC

1. INTRODUCTION

Since the discovery of a high temperature superconductor [1-3], lots of effort has been made to realize the HTS applications [4-15]. Among the applications, those using the HTS tape are expected as the promising ones, since the tapes could give a flexible shape for variable performances corresponding to the applications.

There has been two different kinds of HTS tapes, which are BSCCO silver sheathed tapes and Y(RE)BCO coated conductors (CCs). Although the development of the BSCCO tapes was preceded [11, 12], a large-scaled development of CCs was started in FY1999 as the national project in Japan with expectation of the high potential of REBCO material [5, 8, 10]. Through the development of CCs of ten years, a length ($L$) and a critical current ($I_c$) had been drastically improved and the reasonable long tapes with high performance typically such as 200m-200 A/cm-width could be prepared [5, 9, 10]. This led to the start of a new national project of the M-PACC project [4, 6]. In this project, verifications of electric power application devices such as a transmission cable, a transformer and a superconducting magnetic energy storage (SMES) system using CCs were carried out as the sub-projects. In parallel, the further development of CCs was also included as another sub-project.

In this paper, the brief summary of the electric power device verifications and the detailed achievements on the R&D of CCs in the M-PACC project were reviewed.

2. BRIEF SUMMARY OF ELECTRIC POWER DEVICES

The objectives of the M-PACC project are to develop next-generation superconducting electric power devices with CCs and to establish important component technologies to realize a more stable and highly efficient electric power supply system as a part of the economic infrastructure of our society.

2.1. Superconducting Transmission Cable [4, 16, 17]

The cables to be developed in the M-PACC project are relatively shorter in length (e.g. 15, 30 m than those in other projects; however, their development was very challenging in terms of the cable specifications such as high applied voltage and large current capacity. The two different cables with different specifications had been developed as a three-core (3-IN-ONE\textsuperscript{a}, Triad) 66 kV/5 kA large-current cable system and a single-core 275 kV/3 kA high-voltage cable system.

Regarding the development of large-current and low-AC-loss cables, a three-phase model cable was designed and fabricated, with a core consisting of four layers of superconducting CCs for the conductor and two
layers for the shield. The total AC losses of the conductor and the shield at 71 K were 0.4 W/m/phase at 5 kA was confirmed in the cable using 2 mm wide tapes.

For high-voltage and low-dielectric-loss cables, the technologies for insulating to high voltage, joint and reduction of losses were developed. As a typical result, the AC loss of a single-core model cable made of 3 mm wide tapes were reduced to 0.2 W/m/phase for conducting layer with shielding one. On the other hand, the dielectric loss was as low as 0.6 W/m with the use of polypropylene-laminated paper, PPLP®-C as the insulation material of the choice.

2.2. Transformer [4, 18]
In this project, the several important technologies aiming at a 20 MVA were investigated and a 2MVA transformer using CCs were designed, fabricated, and verified as a demonstration device.

A 400 kVA model transformer with a function of fault current limiting was constructed and tested. The results showed no degradation of the model transformer, including its coils, CCs, and joints, after the short-circuit test, with applied overcurrents being 6 times larger than the rated currents of 174 A at 66 K.

In order to demonstrate the low AC loss in a coil form, a model coil with a scribed conductor was fabricated and tested. The coil was made of a single piece of a scribed five-filament conductor, with the length of 70 m. It was wound 10 times for each layer, and 19 layers to make up a coil. The AC loss of the model coil so constructed was one-fifth of that in the case with a CC without scribing.

A 66kV/6.9kV 2MVA class superconducting transformer was designed including a high-efficiency and compact reverse Brayton cycle refrigerator. On the basis of the design, such a system was constructed and the expected performance was confirmed.

2.3. SMES [4]
A 2 GJ class large-capacity SMES system to be developed in the M-PACC Project was expected to meet the requirements for stable and compact electric power grid control system technology. The main issues in the 2 GJ class SMES system are the development of large-current-capacity conductors made of YBCO CCs and the realization of compact coils wound using large-capacity conductors.

They manufactured component coils (outer diameter 650 mm), adopting a bundled conductor that would enable the realization of high strength and low loss, proving that the coils’ current capacity was sufficiently large, exceeding 2.6 kA, through a basic verification test for energization. In addition, the unique coil construction, “Yoroi-coil”, was developed and it could realize high proof abilities against both the thermal stresses and the electromagnetic hoop stress.

3. THEMES & GOALS OF CCs DEVELOPMENT
Among the specifications required for the future applications such as cable, transformer and SMES on the marketable stage, the similar issues are overlapped such as high in-field performance, low AC loss etc. Then, the issues to be developed were classified into 5 categories and those were set as the themes in this sub-project of CCs development. The targeted goals in this sub-project were determined based on the requirements from the future developments as shown in Table 1 [6].

Table 1

<table>
<thead>
<tr>
<th>Themes</th>
<th>Goals</th>
</tr>
</thead>
<tbody>
<tr>
<td>Degradation of Properties</td>
<td>Evaluation of degradation by Humidity etc.</td>
</tr>
<tr>
<td>High $I_c$ under Magnetic Fields</td>
<td>$500$A/cm-w @77K, $3T$ – $200$m</td>
</tr>
<tr>
<td>Low AC Loss</td>
<td>$2$mm-w – $500$A/cm-w @77K, s.f.-$200$m</td>
</tr>
<tr>
<td>High Strength &amp; High $J_e$</td>
<td>$J_e=500$A/cm2 @50$m$</td>
</tr>
<tr>
<td>Low Cost &amp; High Yield</td>
<td>Low cost toward $20$/kAm</td>
</tr>
</tbody>
</table>

Fig. 1. $I_c$ degradation due to humid atmosphere at different temperature.
the investigation as shown in Fig.2, it was found that the correlation between the delamination strength and the delaminated interface was classified into three groups. In the group of low strength range around 10 MPa, all samples have the bed layer of Gd-Zr-O fabricated by an MOD process and the layer appeared in the delaminated interface. Then, the starting point was estimated in the MOD bed layer. On the other hand, the starting point of middle strength range around 30 MPa was considered to occur at the interface between a metal substrate and the bed layer. If the origins of the above mentioned starting points can be eliminated, the tapes should reveal a high delamination strength over 50 MPa, with a starting point in the REBCO layer. Based on this idea, the influences of the process conditions had been investigated and found. Then, it was found that the increment of the temperature of the heat treatment for the MOD bed layer and reduction of the deposition time for the PLD process could make the delamination strength higher. Finally, the tapes without low and middle strengths were successfully fabricated.

4.2. High \(I_c\) under Magnetic Fields

As a requirement for magnetic applications such as SMES etc., the superconducting performance under magnetic fields should be improved. In this theme, the in-field \(I_c\) had been improved by the introduction of artificial pinning center (APC) and/or thickening the superconducting film layers.

In the early stage of the development in the PLD process for superconducting layers, the BaZrO\(_3\) (BZO) nano-rods [19, 20] were successfully dispersed and the \(J_c\) and/or \(I_c\) performance under the magnetic field, especially for the magnetic field parallel to the c-axis of the GdBCO crystals in the films, has been improved. However, the in-field \(I_c\) values in thick films was saturated in the thickness dependence of \(I_c\). For this problem, BaHfO\(_3\) was found to be effective for maintaining the liner relationship between in-field \(I_c\) and the GdBCO layer thickness up to 3\(\mu\)m [4, 21, 22]. Then, a high \(I_c\) value of 85 A/cm-width at 77 K and 3 T was realized in the BHO doped GdBCO film as shown in Fig.3. According to the TEM observation, nano-rods of BHO are shorter comparing with BZO and inclined from the c-axis of GdBCO films. Additionally, the morphologies of the BHO nano-rods were uniform from the bottom near the CeO\(_2\) layer to the surface. Reflecting these features, the BHO doped GdBCO films reveal the isotropic behavior in \(J_c\)-B-\(\theta\) properties and the difference in the shape could not be recognized for the different thicknesses. Additionally, another advantage of this system was also found. Although the advantages in \(J_c\)-B properties to non-doped films at high temperature disappear at low temperature region in the case of BZO doped GdBCO films, those in the BHO doped films is maintained in wider range of temperatures and magnetic field.

Then, it was found that higher \(J_c\) properties with maintaining a linier relationship in the thickness dependence could be obtained in the combination of the EuBCO superconducting matrix with BHO as APC. In the case of the EuBCO-BHO system, it was found that the fraction of the current disturbing particles, such as a-axis oriented grains, CuO etc., in the film was reduced comparing with GdBCO-BHO one. Finally, an extremely high in-field \(I_c\) value of 141 A/cm-width at 77 K and 3 T was confirmed(Fig.3). This sample was evaluated in a wide range of temperatures and magnetic fields. A 200 m long tape with BHO doped EuBCO is successfully fabricated and a high in-field \(I_c\) value of 54 A/cm-width at 77 K and 3 T was recognized as a minimum value. Most recently, a higher minimum \(I_c\) value of 108 A/cm-width at 77 K and 3 T was obtained in a 94 m long tape.

![Fig. 2. Observation of delaminated interfaces and the estimated starting points of delamination.](image)

![Fig. 3. Thickness dependence of \(I_c\)-min. at 77 K under 3 T for different RE in the superconducting layer and APC materials.](image)
nano-particles. This process is called as the interim effective for fine and uniform dispersion of BZO corresponding to each application.

Therefore, if the cable is made with a perfect round shape, current flow should generate along the surface of the cable. For a transmission cable, the magnetic field due to the installation of application devices such as a drawing process in the transmission cable. In this theme, the strengthening technologies of the metal substrate had been developed and the process conditions for them had been adjusted.

The mechanical strength was improved by modification of deformation process of the Hastelloy\textsuperscript{TM} substrate. As a result, the tensile strength of 1GPa was confirmed by the tapes cut from a 200m long tape with a high $I_c$ value over 500 A/cm-width. This result met the targeted goal shown in Table 1.

Additionally, a high engineering current density is necessary to realize compact devices. Especially for an underground cable, a high $J_e$ tapes are strongly required since the space of the duct for installation of the cable is limited. A thin metal substrate of 70\textmu m in thickness was prepared. The buffer and superconducting layers were deposited on a 217 m (70 \textmu m) metal tape with low tension and a high $I_c$ value of 539 A/cm-width at 77K, self-fields was confirmed. It corresponds to the $J_c$ value of 52 kA/cm$^2$, which is also higher than the targeted goal.

4.4. High Strength & High $J_e$

A large electro-magnetic hoop stress could occur in the high magnetic field applications such as a SMES and a mechanical stress could be loaded for production and industrialization. Concerning the development of the scribing technique, the chemical etching process was optimized to minimize the over-etching region into the REBCO films. As a result, a 100 m long tape with 5 mm in width was divided into 10 filaments and the AC loss reduction of 1/10 was confirmed. This satisfied the targeted goal of the AC loss reduction.

Fig. 4. $I_c$ distributions of the long tapes without (a) and with (b) cleaning of CeO$_2$ buffer layer.

Fig. 5. Zr-element mapping image in the cross sectional image of the films observed by TEM with (a) and without (b) interim annealing.

In the case of AC applications, the reduction of AC losses is one of the important factors. Even when the magnetic field is changed, e.g. ramping step even in the DC magnet, a similar hysteresis loss could be a problem to be solved due to its heat generation. In this theme, the technologies to reduce the losses had been developed corresponding to each application.

For a transmission cable, the magnetic field due to the current flow should generate along the surface of the cable. Therefore, if the cable is made with a perfect round shape, the hysteresis loss should not be a problem due to an extremely thin superconducting layer. However, when tape-shaped conductors are used, the edge of the tapes has to be off the circle. Therefore, narrowing the tape width is effective to fit the core for making a circular shape. In this theme, we had made efforts to fabricate CCs with high uniformity and developed cutting technologies without any serious damages to the superconducting films are required to realize both high performance and low loss properties. On the development of the scribing technique, the mechanical etching process was optimized to minimize the over-etching region into the REBCO films. As a result, a 100 m long tape with 5 mm in width was divided into 10 filaments and the AC loss reduction of 1/10 was confirmed. This satisfied the targeted goal of the AC loss reduction.

In order to reduce the loss for transformer, in which the transverse magnetic fields to the tapes, it is known that the narrowing the width of the tapes such as filamentation is effective. Then, the two dimensional uniformity of the $I_c$ performance is also necessary. And a scribing technique to remove particle defects. It results in the improvement of the performance uniformity as shown in Fig.5. On the other hand, a cutting technology using a YAG-laser was developed and the condition was optimized for minimizing the damage of the tape. Finally, a high $I_c$ performance was realized in a 200 m long with 2 mm wide tape, which satisfied the targeted goal.

4.3. Low AC Loss

In the case of AC applications, the reduction of AC losses is one of the important factors. Even when the magnetic field is changed, e.g. ramping step even in the DC magnet, a similar hysteresis loss could be a problem to be solved due to its heat generation. In this theme, the technologies to reduce the losses had been developed corresponding to each application.

For a transmission cable, the magnetic field due to the current flow should generate along the surface of the cable. Therefore, if the cable is made with a perfect round shape, the hysteresis loss should not be a problem due to an extremely thin superconducting layer. However, when tape-shaped conductors are used, the edge of the tapes has to be off the circle. Therefore, narrowing the tape width is effective to fit the core for making a circular shape. In this theme, we had made efforts to fabricate CCs with high uniformity and developed cutting technologies with low damage. For these targets, it was found that cleaning process of the buffer layer is effective to remove particle defects. It results in the improvement of the performance uniformity as shown in Fig.5. On the other hand, a cutting technology using a YAG-laser was developed and the condition was optimized for minimizing the damage of the tape. Finally, a high $I_c$ performance was realized in a 200 m long with 2 mm wide tape, which satisfied the targeted goal.
production rate and $I_c$ property is important. In the PLD process, it was found that the higher laser energy density and the high pressure are effective to obtain higher production rates. On the other hand, the interim annealing, which is effective to make BZO particles finer, is also valid to improve $J_e$ property even in the self-field. Then, the low technical cost of 1.6 Yen/Am was achieved both in the PLD and MOD CCs. As an example, Figure 6 shows one of the results obtained in the CC by the TFA-MOD process. In this case, the CC was fabricated at 10m/h for the crystallization step and reveals the higher $I_c$ values than 605 A/cm-width.

**5. ACHIEVEMENT TO GOALS**

Finally, all targeted goals of the themes in this project, which is indicated in Table 1, were successfully achieved as shown in Table 2.

<table>
<thead>
<tr>
<th>Themes</th>
<th>Goals</th>
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<tbody>
<tr>
<td>Degradation of Properties</td>
<td>Limiting period in the worst storage condition = about 9 years</td>
</tr>
<tr>
<td></td>
<td>Clarification of the delamination mechanism</td>
</tr>
<tr>
<td></td>
<td>$\Rightarrow$ Elimination of origins for delamination $\Rightarrow &gt; 50$ MPa</td>
</tr>
<tr>
<td>High $I_c$ under Magnetic Fields</td>
<td>(PLD) 54 A/cm-w @77 K, 3 T − 200 m</td>
</tr>
<tr>
<td></td>
<td>770 A/cm-w@77 K, 0.1 T − 158 m</td>
</tr>
<tr>
<td></td>
<td>(MOD) 50 A/cm-w@77 K, 3 T − 124 m</td>
</tr>
<tr>
<td></td>
<td>524 A/cm-w@65 K, 0.1 T − 100 m</td>
</tr>
<tr>
<td>Low AC Loss</td>
<td>(PLD) 2 mm-w − 540 A/cm-w @77 K.s.f.-200 m</td>
</tr>
<tr>
<td></td>
<td>(MOD) 4 mm-w−590 A/cm-w@77 K.s.f.-80 m</td>
</tr>
<tr>
<td></td>
<td>(PLD&amp;MOD) 5 mm-w &amp; 10 filaments $\Rightarrow$ Loss Reduction to 1/10</td>
</tr>
<tr>
<td>High Strength &amp; High $J_e$</td>
<td>(PLD) &gt;500 A/cm-w @77 K.s.f. − 1 GPa−200 m</td>
</tr>
<tr>
<td></td>
<td>$J_e=52$ kA/cm$^2$−200 m</td>
</tr>
<tr>
<td>Low Cost &amp; High Yield</td>
<td>(PLD) $16$/kAm 604 A/cm-w@30 m/h</td>
</tr>
<tr>
<td></td>
<td>(MOD) $16$/kAm 605 A/cm-w@5,10 m/h</td>
</tr>
</tbody>
</table>

**6. CURRENT STATUS IN THE WORLD**

In this section, the above results in the M-PACC project are compared with the current major results in the world. Concerning the evaluation of degradation, there has been few systematic analysis on the influence of a high humidity atmosphere and a delamination behavior. Especially for the delamination, the progress in this project, such as understandings of the mechanism and development of a counter-plan process to eliminate the origins of low delamination strength, are the only results in the world.

In the subject of in-field $I_c$ improvement, lots of results have been reported by many institutions. Recently, SuperPower Inc. reported the improved results of the in-field $I_c$ values in a short tape such as 1353 A/cm-w at 50 K under 3T by increment of BZO concentration into GdBCO film by the IBAD-MOCVD process [25]. However, that in the long tape was still low as 14 A/cm-w at 77 K under 3 T. In the MOD tapes, the result by the company of amsc has stayed at <20 A/cm-w at 77 K under 3 T.

As a comparison for the vapor-deposited tapes, the highest $I_c$ value at 77K under 3T of 141A/cm-w in this project corresponds to 1400A/cm-w at 50K under 3T, which is equivalent to that reported by SuperPower Inc. However, the long tape performance by this project is 4 times higher. Additionally, that of MOD tapes by this project has a similar advantage to that by amsc.

On the other hand, there has been a few reports on the reduction of AC loss for the tapes. SuperPower Inc reported several different techniques to make filaments in the tapes and successfully divided the 12 mm wide tape into 12-filaments, which reveals AC loss reduction to 1/12 [26]. This result is equivalent to that obtained in this project for a short tape. However, the AC loss reduction in a long tape could not been reported by any institutions except for the results in this project.

**7. FUTURE PROSPECTS**

Through the development in the M-PACC project, lots of progress had been achieved as mentioned above. Some prototype devices of the applications will be realized if the achievements can be effectively applied. However, in order to realize the superiority with absolute certainty to the competitive technologies, much higher specifications of CCs are required such as higher $I_c$ under self-field and external magnetic fields etc. which is so-called as “3rd generation wire”. For example, the extremely high uniformity in $I_c$ properties is two dimensionally needed in the next project of “Development of HTS coiling technology”. In the new project, the extremely homogeneous field in the relatively large region such as 40 cm³ has to be realized for medical use as an MRI and heavy ion medical accelerator. In the next stage of HTS development, further development of CCs towards 3rd generation wire as well as HTS device development from a
small test type to a large prototype should be promoted in parallel.

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