Operation characteristics of a fault current limiter by high speed interrupter and a superconducting element

I. G. Im, I. S. Jung, and H. S. Choi

Chosun University, Gwangju, Korea

(Received 18 March 2014; revised or reviewed 24 September 2014; accepted 26 September 2014)

Abstract

Due to continuous increase of electric power consumption, couple of resolutions for improving accuracy in power system like line separation are being studied. The increase of the power demand can cause problems such as supply difficulties of the electricity and broadband outages, failure, etc. When a fault occurs in the power system, a fault current also increases. Fault current creates problems like reduction of lifespan and failure on the power system. In order to resolve these problems, the reduction of initial fault current using the characteristics of superconducting element was applied to fault current limiter. We applied the system to high speed fault current limiter. We found that the superconducting element effectively reduced initial fault current and the fault current was limited by changing operation of high speed interrupter.

Keywords: Interrupter, Superconducting, Fault Current Limiter, Quench

1. INTRODUCTION

Steady increase in the volume of demands for power contributes to increased construction of power facilities. The increased power facilities are designed to have a network system structure to improve power quality through enhanced supply reliability. However, grid connection in this structure reduces total impedance in a power network to make great ground fault current flow when a failure occurs. If the magnitude of the increased ground fault current exceeds the breaking capacity of existing breakers, it may propagate troubles to the breakers and power equipment around the troubled area. Furthermore, there is a greater possibility of secondary accidents like power failures in wide areas[1-5]. As the breaking level of breakers is exceeded to result in reduced power supply reliability and stability, there is an urgent need of tackling the issues. One of the solutions is to replace old breakers by new breakers of great capacities. However, technical constraints and repeated breaker replacement to meet the demand for increasing power are a great economic burden and it is not a fundamental solution[6-8]. Therefore, studies of fault current in relation to reduction have been done, and one method is to study limiters by means of high temperature superconductors. The superconductors feature critical temperature, critical magnetic fields and critical current, and their electric resistance under the critical values is zero. The superconducting fault current limiter uses critical temperature which is one of critical features and supplies power to loads in the system without ohmic loss[8-11]. However, it is necessary to build a cooling system to keep superconductivity, and a lot of expenses are required to maintain the system. Applying a superconducting limiter to a system to endure ground fault current can reduce the limiter lifespan. Therefore, it is possible to reduce deterioration of superconductors if they limited only ground fault current. This study thus suggested a high speed ground fault current limiter for protecting breakers and power equipment by reducing the ground fault current quickly, concurrently bypassing and limiting the ground fault current to the current limiter. In this study, a simulation system was built to apply a high speed ground fault current limiter to a distribution system. We applied superconducting fault current limiter. It limited the initial fault current. In addition, the time for the back protection operation could obtain due to longer fault duration, when the circuit breaker was not opened. So, We of power capacity and operation characteristics of a superconductor when the system has a failure to ensure the reliability of the power system.

2. DETAILED DESCRIPTION

2.1. Specifications and characteristics of a superconductor used in test

Fig. 1 shows a meander-line superconductor. In this test, a meander-line type thin YBCO film was used, which was deposited on a sapphire substrate. Table 1 shows the specifications of the superconducting thin film. The produced superconductor was 540 mm in length shaped a meander line on a substrate which is 2 inches in diameter and 2mm in width. The critical temperature was 87-88K,
TABLE I
SPECIFICATIONS OF A SUPERCONDUCTOR.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Diameter</td>
<td>50.8</td>
<td>mm</td>
</tr>
<tr>
<td>Width</td>
<td>2</td>
<td>mm</td>
</tr>
<tr>
<td>Length</td>
<td>540</td>
<td>mm</td>
</tr>
<tr>
<td>The critical temperature</td>
<td>87-88</td>
<td>K</td>
</tr>
<tr>
<td>The critical current density</td>
<td>3</td>
<td>MA/cm²</td>
</tr>
<tr>
<td>Au Thickness</td>
<td>0.1</td>
<td>μm</td>
</tr>
</tbody>
</table>

and the critical current density was 2-3 MA/cm². Fig. 2 shows the critical current values of the used superconductor. It was shown that quenching occurred at 19 A and current was gradually increased when 1 V of a voltage source was applied. This was to sharply increase impedance of the superconductor to limit current, and was in charge of reducing the burden on protection facilities and power devices by limiting initial ground fault current when a fault was applied in this test.

2.2. Configuration of high speed interrupter and test of applying superconductor

Fig. 3 shows the structure of a produced high speed interrupter. The high speed interrupter was made by combining a solenoid valve with a vacuum interrupter, and was in charge of bypassing the ground fault current to a line to which the current limiter was connected when the fault occurs on the line. Tables 2 and 3 show the specifications of the solenoid valve and the vacuum interrupter. The solenoid valve was selected in consideration of ideal suction power because it should be able to open contacts when faults happen. The vacuum interrupter shown in Table 3 was the one verified to have an ideal insulation level to be applied to a distribution system. Before testing each type of system failures, a single phase test was performed for which the superconductor was applied to the high speed interrupter. In this test, an analysis was made of limiting characteristics of initial ground fault current depending on application of the superconductor to a normal
line. Fig. 4 shows a circuit for testing the characteristics of ground fault current depending on application of the superconductor. Case 1 is for no application of the superconductor, and Case 2 is for application of the superconductor. Fig. 5 shows characteristics of initial ground fault current where the superconductor was applied. When the circuits shown in Case 1 and Case 2 of Fig. 4 were made to have an intentional failure, Case 1 showed sharply increasing current values to maximum 266.77 A. On the other hand, a superconductor was applied in Case 2, it was identified that ground fault current was effectively limited to 29.02 A in a short period of time after having a trouble. The high speed ground fault current limiter is a system for changing and then bypassing a line while quickly limiting ground fault current coming into power facilities when a system has a trouble, in order to limit continuous ground fault current. This test aimed to simulate single, double and triple line-to-ground fault in a system to analyze burden of power and the characteristics of the superconductor for limiting initial ground fault current.

![Fig. 4. Circuit for testing characteristics of ground fault current depending on application of a superconductor. (a) Case. 1: Superconductor not applied (b) Case. 2: Application of a superconductor](image)

![Fig. 5. Characteristics of initial ground fault current depending on case1 and case 2.](image)

3. Experiments

3.1. Organization and test results

![Fig. 6. Test circuit of high speed ground fault current limiter.](image)

![Fig. 7. Electrical characteristics in single line-to-ground fault.](image)

![Fig. 8. Electrical characteristics in double line-to-ground fault.](image)
I. G. Im, I. S. Jung, and H. S. Choi

Fig. 9. Electrical characteristics in triple line-to-ground fault.

Fig. 6 shows test circuit for which a high speed ground fault current limiter was applied. In the normal state, current was supplied to the load through b-contact of the high speed interrupter and the superconducting element with no resistance when power voltage of 400 V was applied to each phase through SWR, SWS and SWT. Where single, double and triple line-to-ground faults was generated by using fault switches connected to a load in parallel, the magnitude of the ground fault current increased tens of times in comparison with normal current. In this case, when the CT sensed ground fault current and the SCR control system applied power to the high speed interrupter, the line was changed to a current limiter. The initial ground fault current coming before the lines changed was limited by a superconductor.

Fig. 7 shows the current limiting characteristics in single line-to-ground fault. When single line-to-ground fault occur through the switch SWR, ground fault current sharply increase and the superconductor quenched if the increased ground fault current exceeds the critical current value of the superconductor. In this case, the magnitude of ground fault current was limited to 29.12 A due to the generated impedance of the superconductor. The burden of power laid on the superconductor was identified 3194.44 W, 3270.85 W and 3180.14 W, respectively. The high speed interrupter changed the line to which the current limiter was connected after 12.17 ms, 11.44 ms, 12.16 ms to limit ground fault current. When the fault occurred. The result of test revealed that ground fault current came into the normal line more than 1 cycle when the superconductor was not applied. Therefore, the superconductor was inserted in a normal line in order to protect protection facilities, for example, a circuit breaker, by effectively limiting initial ground fault current. The ground fault current was limited within a half cycle after a fault occurred. The line change by the high speed interrupter contributed to stably limiting ground fault current by impedance and also to reducing the burden of the superconductor.

Fig. 8 shows the current limiting characteristics in double line-to-ground fault. While ground fault current in phase R was limited to 29.12 A, ground fault current in phase S was limited to 47.65 A, when double line-to-ground fault occurred through switch SWR, and fault SWs. This is because the fault was simulated on the basis of 0 degree of phase R. In this case, because the current value of phase S was the greatest value by the phase difference with phase R, ground fault current became greater. However, it was shown that the ground fault current was quickly limited due to the quenched superconductor, and the burden of power laid on the superconductor was 3270.85 W and 3261.28 W for phases R and S, respectively. The changing time of the high speed interrupter after the fault was 11.59 ms, 11.24 ms.

Fig. 9 shows current limiting characteristics in triple line-to-ground fault. Ground fault current was limited from 29.12 A, 47.65 A, 35.88A for phases R, S and T, respectively, when triple line-to-ground fault occurred through switch SWR, switch SWs, and switch SWT. In this case, burden of power laid on the superconductor for each phase was 3194.44 W, 3428.64 W, and 3180.14 W, respectively. The high speed interrupter changed the line to which the current limiter was connected after 12.17 ms, 11.44 ms, 12.16 ms to limit ground fault current. When the fault occurred. The result of test revealed that ground fault current came into the normal line more than 1 cycle when the superconductor was not applied. Therefore, the superconductor was inserted in a normal line in order to protect protection facilities, for example, a circuit breaker, by effectively limiting initial ground fault current. The ground fault current was limited within a half cycle after a fault occurred. The line change by the high speed interrupter contributed to stably limiting ground fault current by impedance and also to reducing the burden of the superconductor.

4. CONCLUSION

This study aimed to establish a method of quickly limiting initial ground fault current and reducing the burden laid on a superconductor when a system failure occurs in order to address issues, for example, excessive breaking capacity currently caused by increased demands for power. Although typical superconducting limiters already available are excellent in terms of fault limiting capability, they had issues of burden of power and capacity increase to address continuous ground fault current, and required cooling facilities. Accordingly, this study suggested a method of changing lines to limit initial ground fault current by impedance while a superconductor quickly limits it, and to make an analysis of power burden and operation characteristics laid on the superconductor.

The result of test revealed that 266.77 A of ground fault current came into the normal line more than 16 ms when the superconductor was not applied. But, when applied to the superconductor fault current, Initial fault current was limited to 29.12A, and the line has been change after 12 ms. It was thought that a high speed interrupter could bypass ground fault current to reduce burden laid on a superconductor. This process can contribute to protecting breakers of which the capacity is excessive when the method is applied to a distribution system, and to tackling failure propagation while addressing the issues of increasing superconductor capacity and reducing cooling facilities.
ACKNOWLEDGMENT

This research was financially supported by the Ministry of Education, Science Technology (MEST) and National Research Foundation of Korea(NRF) through the Human Resource Training Project for Regional Innovation (No. NRF-2013H1B8A2032246).

REFERENCES


