Sliding Mode Control with Fixed Switching Frequency for Four-wire Shunt Active Filter

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Abstract – The present paper proposes a sliding mode control with fixed switching frequency for three-phase three-leg voltage source inverter based four-wire shunt active power filter. The aim is to improve phase current waveform, neutral current mitigation, and reactive power compensation in electric power distribution system. The performed sliding mode for active filter current control is formulated using elementary differential geometry. The discrete control vector is deduced from the sliding surface accessibility using the Lyapunov stability. The problem of the switching frequency is addressed by considering hysteresis comparators for the switched signals generation. Through this method, a variable hysteresis band has been established as a function of the sliding mode equivalent control and a predefined switching frequency in order to keep this band constant. The proposed control has been verified with computer simulation which showed satisfactory results.

Keywords: Sliding mode control, Switching frequency, Four-wire active filter, Current harmonics compensation

1. Introduction

At present, the three-phase four-wire active power filters are attracting considerable interest of researchers in the field of power quality conditioning [1]-[9]. Two configurations of voltage source inverter (VSI) can be used to implement three-phase four-wire active filters. The first uses a fourth leg to provide the neutral current. The second configuration uses a conventional three-leg converter with two cascade-connected capacitors in the DC-bus. The neutral wire is connected directly to the midpoint of this bus. Although the four-leg configuration is preferable because of its simple controllability [5], [10], the three-leg configuration which is used in this paper has the advantage of reduced number of semi-conductors.

The computation of compensated components is the first step required in active filter control. It is used to identify the undesirable component to be suppressed and consequently, the additional components needed to compensate the active filter losses. Several control algorithms, such as instantaneous reactive power theory [11], synchronous reference frame [7], and fast Fourier transform method [12] are used in this manner. Subsequently, the VSI is forced to inject these components in the point of common coupling with minimum error and fast response. This requires an appropriate current control method. In this way, the sliding mode control (SMC), which is derived from the theory of variable structure control and known as a discontinuous control technique that takes into account the time varying topology of the controlled system, is naturally suitable to the control systems. Suitability is based on power electronics devices in general [13]-[18] as well as active filter as a particular case of these systems [19]-[21]. Sliding mode control is characterized by simple implementation, fast response, and high robustness. However, the ideal sliding motions imply infinite frequency in the switched signals which is naturally impossible to achieve in practice, where the switching frequency must be finite and stable. The idea of fixed frequency control in power converters has been addressed in several works [22]-[28]. Most of these contributions are based on the relation between the switching frequency and the average control which is also called equivalent control. It has been shown that it is possible to modulate this continuous control to generate the switched signals with fixed frequency by using PWM modulation [23], [29] or Σ-Δ-modulation [18]. In the present paper, the switching signals are generated through hysteresis comparators, which are very popular in current control application and often used in sliding mode implementation, in order to limit the switching frequency. Although simple and extremely robust [30], the switching frequency remains free and varies considerably with respect to the state variables when the hysteresis bandwidth is preliminary fixed.

However, it has been shown in several studies [22], [24]-[27], [30] that it is possible to keep this frequency constant by adopting a variable hysteresis bandwidth. The idea developed in this work consists of implementation of the sliding mode control with varied hysteresis bandwidths.
instead of the switching frequency which is required to be constant.

This paper is divided as follows. A brief description and modeling of the active filter are presented in section 2 while the principle of the compensated components computation is analyzed in section 3. The aim of the paper is discussed in detail in section 4 and the computer simulations are given in section 5. The last section provides the conclusion.

2. System Description and Modeling

Fig. 1 illustrates the basic compensation principle of the four-wire shunt active power filter (SAPF). The power circuit is based on three-phase three-leg controlled current voltage source PWM inverter connected to the grid at the AC side through a passive filter \((L_c, r_c)\). The circuit uses two cascade connected capacitors \(C_1 = C_2 = C\) as voltage source at the DC side, with the midpoint connected to the neutral wire of the grid to compensate neutral load current. An unbalanced nonlinear load is considered as a polluting source that draws unbalanced and distorted current \(i_{abc}\) from the main source. The SAPF is controlled to inject compensated current vector \(i_{abc}\) in the grid in order to achieve source current \(i_{abc}\) which is balanced, sinusoidal, and in phase with the fundamental main voltages, while keeping the DC-link voltages \(V_{C1}\) and \(V_{C2}\) balanced and within an admissible range. To establish the dynamic equations of the system, let us suppose that the power switch \(S_j\) can be assumed as ideal. Consequently, the output voltage for each phase \(j\) to neutral can be expressed as follows [31]:

\[
v_{cj} = d_j V_{C1} - \bar{d}_j V_{C2} \tag{1}
\]

\(d_j (j = a, b, c)\) are the PWM switching functions given by:

\[
d_j = \frac{u_j + 1}{2} \tag{2}
\]

where \(u_j\) is associated to the power switch states as follows:

\[
\begin{align*}
u_j &= 1 \text{ If } S_j\text{on and } S'_j \text{ off} \\
u_j &= -1 \text{ If } S_j\text{off and } S'_j \text{ on.}
\end{align*}
\]

Substituting Eq. (2) for Eq. (1), the active filter voltage is subsequently rewritten as follows:

\[
v_{cj} = \frac{1}{2} u_j (V_{C1} + V_{C2}) + \frac{1}{2} (V_{C1} - V_{C2}). \tag{3}
\]

The DC-bus voltages across the two capacitors are related to \(u_j\) and the active filter currents \(i_{cj}\) is written as follows:

\[
\frac{dV_{C1}}{dt} = \frac{1}{2C} \left( \sum_{j=a,b,c} u_j i_{cj} + \sum_{j=a,b,c} i_{cj} \right) \tag{4}
\]
From Kirchhoff’s voltage law, the interaction between the voltage source inverter and the grid is described by the following differential equation:

\[
\frac{dV_{c2}}{dt} = \frac{1}{2C} \left( \sum_{j=a,b,c} u_j i_{cj} - \sum_{j=a,b,c} i_{cj} \right),
\]

(5)

where \( i_j \) represents the main voltages at the point of common coupling. Finally, these equations are rearranged under matrix form:

\[
\dot{x} = Ax + Bu + v
\]

(7)

where

\[
A = \begin{bmatrix}
-\frac{r_c}{L_c} & 0 & 0 & \frac{1}{2L_c} & -\frac{1}{2L_c} \\
0 & -\frac{r_c}{L_c} & 0 & \frac{1}{2L_c} & -\frac{1}{2L_c} \\
0 & 0 & -\frac{r_c}{L_c} & \frac{1}{2L_c} & -\frac{1}{2L_c} \\
\frac{1}{2C} & \frac{1}{2C} & \frac{1}{2C} & 0 & 0 \\
-\frac{1}{2C} & -\frac{1}{2C} & -\frac{1}{2C} & 0 & 0
\end{bmatrix}
\]

\[
B(x) = \begin{bmatrix}
V_{dc} & 0 & 0 \\
0 & V_{dc} & 0 \\
0 & 0 & 2L_c \\
2C & 2C & 2C \\
2C & 2C & 2C
\end{bmatrix}, \quad u = \begin{bmatrix} u_a \\ u_b \\ u_c \end{bmatrix}, \quad v = \begin{bmatrix} v_a \\ v_b \\ v_c \\ -\frac{v_a}{L_c} \\ -\frac{v_b}{L_c} \\ -\frac{v_c}{L_c}
\end{bmatrix}
\]

3. Principle of Operation

In four-wire systems, the current drawn by an unbalanced nonlinear load contains positive-sequence, negative-sequence, and zero-sequence harmonic components that can be expressed in \( \alpha \beta \gamma \) frames and arranged in matrix form as follows [32]:

\[
i_{\alpha \alpha} = \begin{bmatrix} i_{\alpha 1}^+ & i_{\alpha 2}^+ & \ldots & i_{\alpha n}^+ & i_{\alpha 1}^- & i_{\alpha 2}^- & \ldots & i_{\alpha n}^- \end{bmatrix}
\]

\[
i_{\beta \beta} = \begin{bmatrix} i_{\beta 1}^+ & i_{\beta 2}^+ & \ldots & i_{\beta n}^+ & i_{\beta 1}^- & i_{\beta 2}^- & \ldots & i_{\beta n}^- \end{bmatrix}
\]

\[
i_{\gamma \gamma} = \begin{bmatrix} i_{\gamma 1}^+ & i_{\gamma 2}^+ & \ldots & i_{\gamma n}^+ \end{bmatrix}
\]

In addition, if the main voltages are supposed to be unbalanced and contain harmonics, then they can be expressed in the same way as:

\[
v_{\alpha} = [v_{\alpha 1}^+ \ v_{\alpha 2}^+ \ldots \ v_{\alpha n}^+ \ v_{\alpha 1}^- \ v_{\alpha 2}^- \ldots \ v_{\alpha n}^-]^T
\]

\[
v_{\beta} = [v_{\beta 1}^+ \ v_{\beta 2}^+ \ldots \ v_{\beta n}^+ \ v_{\beta 1}^- \ v_{\beta 2}^- \ldots \ v_{\beta n}^-]^T
\]

\[
v_{\gamma} = [v_{\gamma 1} \ v_{\gamma 2} \ldots \ v_{\gamma n}]^T.
\]

Hence, the instantaneous real, imaginary, and zero-sequence powers absorbed by the nonlinear load are the results from the different interactions between the different harmonics sequences of the load currents and main voltages. Thus, the instantaneous real power components can be expressed in matrix form as follows:

\[
P_L = \begin{bmatrix} v_a \\ v_{-a} \end{bmatrix} [i_a \ i_{-a}].
\]

(8)

This matrix contains all possible combinations between positive and negative-sequences of the main voltages and load currents. It is not difficult to observe that the diagonal elements are in DC form and all the other elements are in AC form. Hence, the DC part of the real power can be expressed as the trace of \( P_L \):

\[
p_L = \sum_{i=1}^{n} (v_{a i}^+ i_{a i}^+ + v_{ai}^- i_{ai}^- + v_{bi}^+ i_{bi}^+ + v_{bi}^- i_{bi}^-).
\]

(9)

After simplification, Eq. (9) can be written in \( abc \) frames as follows:

\[
p_L = p_{LF} + p_{LH}
\]

(10)

where

\[
p_{LF} = 3 V_1^+ I_{1L}^+ \cos(\phi_{1^+} - \phi_{1L}^+),
\]

\[
p_{LH} = \sum_{i=2}^{n} 3 V_i^+ I_{iL}^+ \cos(\phi_{i^+} - \phi_{iL}^+),
\]

\[
+ \sum_{i=1}^{n} 3 V_i^- I_{iL}^- \cos(\phi_{i^-} - \phi_{iL}^-).
\]

\( V_1^+ , V_1^- \) and \( I_1^+ , I_1^- \) are the rms values of the positive and negative-sequences of the voltage and current components for the \( i^\text{th} \) harmonic, whereas \( \phi_{1^+} , \phi_{1L}^+ \) and \( \phi_{1^-} , \phi_{1L}^- \) are their respective phase shifts.

Eq. (10) shows that if the SAPF is controlled to provide constant real power \( p_{LF} \) drawn from the source, then the source currents remain non-sinusoidal because positive and negative sequences of the current that interact with the same sequences at the same frequencies will contribute to a constant real power exchange \( p_{LH} \). Consequently, they are not perceived as undesirable components, thus non-
compensated. In conclusion, to guarantee sinusoidal source current, only $\overline{I}_p$ must be delivered by the source; in other words, the three-phase source currents must contain only fundamental positive-sequence $I_{s1}^*$ of the load currents. However, in the active filter operation, active losses in the power switches and passive filter occur that cause variations in the DC-bus voltage. To avoid this situation, these losses must be compensated by drawing an additional active current $I_{loss}$ from the AC source. This is achieved traditionally by the DC-bus voltage controller that generates the reference signal for $I_{loss}$ from the error between the reference value $V_d^*$ and the measured value $V_d$. Thus, the peak source current including the DC-bus voltage regulation is:

$$I_s = I_{s1}^* \cos (\phi_{s1} - \phi_{t1}^*) + I_{loss}. \quad (11)$$

If $I_{loss}$ is generated by a proportional-integral (PI) controller with $k_p$ and $k_i$ as proportional and integral gains, then:

$$I_{loss} = k_p(V_d^* - V_d) + k_i \int (V_d^* - V_d) \, dt. \quad (12)$$

Subsequently, the resulting instantaneous three-phase source currents are:

$$i_{sa}' = I_s \sin (\omega_f t + \delta_{a1})$$

$$i_{sb}' = I_s \sin (\omega_f t + \delta_{b1})$$

$$i_{sc}' = I_s \sin (\omega_f t + \delta_{c1}) \quad (13)$$

where $\omega_f$ is the fundamental pulsation of the main voltages given by a Phase Locked Loop (PLL); $\delta_{a1}$, $\delta_{b1}$, and $\delta_{c1}$ are the phase shifts of the fundamental main voltages; $\gamma_a$, $\gamma_b$, and $\gamma_c$ are equal to $0$, $-\frac{2\pi}{3}$, and $-\frac{2\pi}{3}$, respectively if the main voltages are balanced. These angles are extracted by a Fourier analysis-based detector. In order to compensate the eventual difference $\Delta V_d$ between the voltage $V_{c1}$ and $V_{c2}$ across the two capacitors $C_1$ and $C_2$ of the DC-bus, the SAPF is forced to absorb a small DC-term current $I_{dc}$ from the AC source. Hence, the instantaneous reference source currents are:

$$i_{sa}' = i_{sa} + I_{dc}$$

$$i_{sb}' = i_{sb} + I_{dc}$$

$$i_{sc}' = i_{sc} + I_{dc} \quad (14)$$

The current $I_{dc}$ is computed directly as follows [8]:

$$I_{dc} = K_{dc}(V_{c2} - V_{c1}). \quad (15)$$

To avoid a large DC-term in the source currents, a slight value for the gain $K_{dc}$ is chosen and an eventual limiter can be required. Thus, this method proposes a Proportional-Integral+Proportional (PI+P) actions for $V_d$ and $\Delta V_d$ control. Finally, the compensating currents can be obtained from the reference source current and the load currents as follows:

$$i_{sa}^* = i_{sa} - i_{sa}$$

$$i_{sb}^* = i_{sb} - i_{lb}$$

$$i_{sc}^* = i_{sc} - i_{lc} \quad (16)$$

4. Sliding Mode Control of the Current Loop

The sliding mode control is derived by selecting the suitable switching configuration of the VSI in order to guarantee the state trajectory attraction toward a predefined sliding surface, and to maintain its stability over this surface. The system established in Eq. (7) is a multi-input multi-output non-linear system. In order to formulate the sliding mode creation problem, let:

$$x = [x_1 \ x_2 \ x_3 \ x_4 \ x_5]^T$$

Subsequently, Eq. (7) can be rearranged as follows [18]:

$$\dot{x} = f(x) + G(x)u \quad (17)$$

where $f(x)$ is a $(n=5)$-dimensional vector field given as follows:

$$f(x) = \begin{bmatrix}
-\frac{r_c}{L_c} x_1 + \frac{1}{2L_c} x_4 - \frac{1}{2L_c} x_5 - \frac{v_{c1}}{L_c} \\
-\frac{r_c}{L_c} x_2 + \frac{1}{2L_c} x_4 - \frac{1}{2L_c} x_5 - \frac{v_{c2}}{L_c} \\
-\frac{r_c}{L_c} x_3 + \frac{1}{2L_c} x_4 - \frac{1}{2L_c} x_5 - \frac{v_{c3}}{L_c} \\
\frac{1}{2c} x_1 + \frac{1}{2c} x_2 + \frac{1}{2c} x_3 \\
-\frac{1}{2c} x_1 - \frac{1}{2c} x_2 - \frac{1}{2c} x_3
\end{bmatrix}$$

$G(x)$ is an $(n \times m = 5 \times 3)$-dimensional input matrix with $m \times n$-dimensional vector field given as follows:

$$G(x) = \begin{bmatrix}
x_{1} & x_{2} & x_{3} \\
x_{4} & x_{5} & x_{6} \\
x_{7} & x_{8} & x_{9} \\
x_{10} & x_{11} & x_{12} \\
x_{13} & x_{14} & x_{15}
\end{bmatrix}$$
4.1 Sliding surfaces

For the \( n \)-dimensional controlled system regulated by \( m \) independent switches, \( m \) sliding surface coordinate functions are defined. To fast track the reference current, the three sliding surface coordinate functions in vector form are defined as follows:

\[
\sigma(x) = \begin{bmatrix} \sigma_1(x) \\ \sigma_2(x) \\ \sigma_3(x) \end{bmatrix} = \begin{bmatrix} x_1 - x_1^d \\ x_2 - x_2^d \\ x_3 - x_3^d \end{bmatrix}. \tag{18}
\]

When the sliding mode is reached, in other words, when the state vector is forced to evolve on the intersection of the sliding surfaces, the sliding surface coordinate function \( \sigma(x) \) must satisfy the following condition:

\[
(\dot{\sigma}(x), \sigma(x)) = (0, 0). \tag{19}
\]

Hence, a sliding mode equivalent control denoted by \( u_{eq}(x) \) may be defined to ensure that the sliding surface coordinate functions \( \sigma(x) \) satisfy simultaneously the following invariance condition [18]:

\[
\dot{\sigma}(x) = \frac{\partial \sigma(x)}{\partial x^T}(f(x) + G(x)u_{eq}(x)) = 0. \tag{19}
\]

We denote \( \frac{\partial \sigma(x)}{\partial x} f(x) \) by \( L_f(x) \), an \( m \)-dimensional vector which represents the directional derivatives of \( \sigma(x) \) along the direction of the vector field \( f(x) \) as shown in Eq. (20).

\[
L_f(x) = \begin{bmatrix} \frac{\partial \sigma_1(x)}{\partial x_1} & \frac{\partial \sigma_1(x)}{\partial x_2} & \cdots & \frac{\partial \sigma_1(x)}{\partial x_5} \\ \frac{\partial \sigma_2(x)}{\partial x_1} & \frac{\partial \sigma_2(x)}{\partial x_2} & \cdots & \frac{\partial \sigma_2(x)}{\partial x_5} \\ \frac{\partial \sigma_3(x)}{\partial x_1} & \frac{\partial \sigma_3(x)}{\partial x_2} & \cdots & \frac{\partial \sigma_3(x)}{\partial x_5} \end{bmatrix} f(x)
\]

where \( L_{fj}(x) \) (\( j = 1, 2, 3 \)) is the \( j \)th component in the vector \( L_f(x) \).

Similarly, the \((m \times m)\)-dimensional matrix \( \frac{\partial \sigma(x)}{\partial x^T} G(x) \) representing the directional derivatives of \( \sigma(x) \) along the directions of the \( m \) vector fields in the matrix \( G(x) \) is denoted by \( L_g(x) \) as shown in Eq. (21). \( L_{g1}(x) \), \( L_{g2}(x) \) are the \( j \)th line and column in the matrix \( L_g(x) \) , respectively.

\[
L_g(x) = \begin{bmatrix} \frac{\partial \sigma_1(x)}{\partial x_1} & \frac{\partial \sigma_1(x)}{\partial x_2} & \cdots & \frac{\partial \sigma_1(x)}{\partial x_5} \\ \frac{\partial \sigma_2(x)}{\partial x_1} & \frac{\partial \sigma_2(x)}{\partial x_2} & \cdots & \frac{\partial \sigma_2(x)}{\partial x_5} \\ \frac{\partial \sigma_3(x)}{\partial x_1} & \frac{\partial \sigma_3(x)}{\partial x_2} & \cdots & \frac{\partial \sigma_3(x)}{\partial x_5} \end{bmatrix} G(x)
\]

Consequently Eq. (19) is rewritten as follows:

\[
\dot{\sigma}(x) = L_f(x) + L_g(x)u_{eq}(x) = 0. \tag{22}
\]

This permits the definition of the equivalent control in the following form:

\[
u_{eq}(x) = -(L_g(x))^{-1}L_f(x). \tag{23}\]

This means that, as a condition for the equivalent control definition, the matrix \( L_g(x) \) must be invertible. It should be noted that the equivalent control must satisfy \(-1 \leq u_{eq}(x) \leq 1\), \( 1 \) represents an \( m \)-dimensional column vector with 1 in each entry) which is a necessary and sufficient condition for the sliding mode existence.

4.2 Sliding surface accessibility

Let us consider the following Lyapunov function:

\[
V(\sigma(x)) = \frac{1}{2} \sigma^T(x)\sigma(x). \tag{24}\]

This positive semi-definite function is identically zero over the surface \( S \), i.e., \( \sigma(x) = 0 \) and positive when \( \sigma(x) \neq 0 \). The quantity \( V'(\sigma(x)) \) can be interpreted as the distance from the position of the point \( x \) in the state space to the desired surface. Therefore, in order to satisfy the condition \( \sigma(x) = 0 \), the discrete control \( u \in \{-1, 1\}^m \) must exercise a closing or opening action, which permits the decrease in the distance \( V(\sigma(x)) \). This means that the variation of this function in relation to time must be strictly negative. Consequently:

\[
\frac{d}{dt} \left(V(\sigma(x))\right) = \sigma^T(x)\dot{\sigma}(x) < 0. \tag{25}\]

This is the condition for the trajectory attraction toward the sliding surface. In Eqs. (22) and (25), if \( \sigma(x) \neq 0 \), and by replacing \( u_{eq}(x) \) by \( u \), then the time derivative of the Lyapunov function can be expressed as follows:

\[
\dot{V}(\sigma(x)) = \sigma^T(x)(L_f(x) + L_g(x)u) < 0. \tag{26}\]
Likewise, if $\sigma(x) \neq 0$, then:

$$\dot{V}(\sigma(x)) = \sigma^T(x)(L_f(x) + L_{Gj}(x)u_{eq}(x)) = 0. \tag{27}$$

If we consider that the switching frequency is infinite or sufficiently high, we can suppose with good approximation that the state vector $x$ takes the same value in both cases of Eqs. (26) and (27). Thus, replacing $\sigma^T(x)L_f(x)$ in Eq. (26) by its value from Eq. (27), the restriction in Eq. (25) can be reformulated as follows:

$$\dot{V}(\sigma(x)) = \sigma^T(x)L_{Gj}(x)(u - u_{eq}(x)) < 0. \tag{28}$$

Hence, from (21) the following inequality can be deduced:

$$\sum_{j=1,2,3}^{} \sigma^T(x)L_{Gj}(x)u_j < \sum_{j=1,2,3}^{} \sigma^T(x)L_{Gj}(x)u_{eqj}(x) \tag{29}$$

Knowing that $-1 \leq u_{eqj}(x) \leq 1$, then (29) can be achieved by applying the following control action:

$$u_j = -\text{sign}\left(\sigma^T(x)L_{Gj}(x)\right) \tag{30}$$

where $\text{sign}$ denotes the sign function.

### 4.3 Switching frequency

The above expressions are rigorously valid only if we suppose that the system is operating with infinite switching frequency. This is an important constraint in the practical implementation of the sliding mode control. In such application, the switching frequency must be fixed and stabilized at a predefined design value. In order to limit this frequency, a commutation law with fixed hysteresis bandwidth is generally used. Let us suppose that for each restriction, $\sigma_j(x) = \sigma_j(x)^* = 0$ is assigned a hysteresis band $\Delta \sigma_j(x)$. Therefore, $\sigma_j(x)$ oscillates between $\pm \Delta \sigma_j(x)^*$, and it is not strictly zero. Consequently, the control defined in Eq. (30) should be redefined as follows:

$$u_j = -\text{sign}\left(\Delta \sigma_j(x)^* - \sigma^T(x)L_{Gj}(x)\right) \tag{31}$$

Thus, the time derivative of the $j^{th}$ sliding surface coordinate function is:

$$\dot{\sigma}_j(x) = L_f(x) + L_{Gj}(x)u_{eq}(x) \tag{32}$$

where $\Delta \sigma(x)^* = [\Delta \sigma_1(x)^* \Delta \sigma_2(x)^* \Delta \sigma_3(x)^*]$ which represents the $m$ hysteresis bandwidths assigned for the $m$ surfaces. If we consider that $\Delta \sigma_j(x)^*$ are sufficiently slight, the switching frequency is therefore sufficiently high. Thus, we can accept with sufficiently approximation that Eq. (32) will be similar to the $j^{th}$ component in Eq. (22) and can be written as:

$$\dot{\sigma}_j(x) = L_f(x) + L_{Gj}(x)u_{eq}(x) = 0. \tag{33}$$

This means that $L_f(x) = -L_{Gj}(x)u_{eq}(x)$, which replaces the term $L_f(x)$ in (32). Thus, the following equations can be deduced:

$$\dot{\sigma}_j(x) = L_{Gj}(x)\text{sign}\Delta \sigma(x)^* - L_{Gj}(x)u_{eq}(x)$$

$$= -L_{Gj}(x)\text{sign}(\sigma(x)^TL_{Gj}(x))^T \tag{34}$$

The new control in Eq. (31) naturally takes two limit values $[-1, 1]$, which are needed to reduce the difference $\Delta \sigma(x)^* - \sigma^T(x)L_{Gj}(x)$ when $\sigma_j(x)^* - \sigma^T(x)L_{Gj}(x)$ is either positive or negative. Consequently, to respect the stability constraint in Eq. (26), Eq. (34) should achieve a positive value in the switched-on interval $t_{on}$ and a negative value in the switched-off $t_{off}$ interval. Therefore, these two intervals can be deduced as follows:

$$t_{on} = \frac{2\Delta \sigma_j(x)^*}{\dot{\sigma}_j(x)} = \frac{2\Delta \sigma_j(x)^*}{L_{Gj}(x)(1 - u_{eq}(x))} \tag{35}$$

$$t_{off} = \frac{2\Delta \sigma_j(x)^*}{\dot{\sigma}_j(x)} = \frac{2\Delta \sigma_j(x)^*}{L_{Gj}(x)(1 + u_{eq}(x))} \tag{36}$$

where $\dot{\sigma}_j(x)$ and $\dot{\sigma}_j(x)$ indicate the positive or negative time derivative of $\sigma_j(x)$ is positive or negative, respectively.

Knowing that the switching frequency is defined as:

$$f_s = \frac{1}{t_{on} + t_{off}} \tag{37}$$

then, the expression of the switching frequency can be deduced from Eqs. (35) and (36) after simplification as follows:

$$f_s = \frac{(L_{Gj}(x)1 - (L_{Gj}(x)u_{eq}(x))^2}{4\Delta \sigma_j(x)^*(L_{Gj}(x)1)} \tag{38}$$

Eq. (38) shows clearly that the switching frequency $f_s$ depends essentially on the equivalent control vector $u_{eq}(x)$ and the predefined hysteresis bandwidth $\Delta \sigma_j(x)^*$. If this last one is fixed at a slight value, then the switching frequency will be increased. This situation can be recommended for harmonic current control. However, in practical applications, this frequency must take a moderate value to limit switching losses and constraints on power switches, especially when the compensated powers are relatively important. Otherwise, the equivalent control is
responsible for switching frequency variations, which can cause audible noise and electromagnetic-related problems. In order to solve this problem, the hysteresis bandwidth \( \Delta \sigma(x) \) in the present paper is modulated as a function of the equivalent control \( \text{equx} \). Thus, this band will change according to the instantaneous value \( \text{s}f \) of the equivalent control in order to maintain a nearly constant switching frequency at a predefined value. This can be seen in the following equation:

\[
\Delta \sigma_f(x) = \frac{\left( L_{G1}(x) \right)^2 - \left( L_{G1}(x) \right)^2 \text{eqx}(x)^2}{4 f_s^2 L_{G1}(x) \text{eqx}(x)}.
\]

Finally, the discrete controls \( u_j \) and \( d_j \) are derived as:

\[
\begin{align*}
    u_j &= - \text{sign} \left( \Delta \sigma_f(x) - \sigma_f^\text{T}(x) L_{G1}(x) \right) \quad (40) \\
    d_j &= \frac{1}{2} (1 + u_j). \quad (41)
\end{align*}
\]

The complete block diagram of the proposed control is shown in Fig. 2.

The performances of the developed sliding mode control are verified through simulation using MATLAB software. The polluting load consists of three-phase thyristor rectifier, single-phase thyristor rectifier, and single-phase diode rectifier. For all the simulations, a load variation is operated at \( t = 0.2 \text{s} \). The main parameters of the system are listed in Table 1.

In Fig. 3, the three-phase and neutral currents are represented with and without active filtering. As illustrated in the same figure, harmonic spectra for each phase for the current drawn by the load is unbalanced and contains positive-sequence harmonics \( 6h+1(7^\text{th}, 13^\text{th} \ldots) \), negative-sequence harmonics \( 6h+5(5^\text{th}, 11^\text{th} \ldots) \), and zero-sequence harmonics \( 6h+3(3^\text{th}, 9^\text{th} \ldots) \), where \( h \) indicates the harmonic order. It should be noted that the zero-sequence harmonics is the result of the 4th wire (neutral). This sequence does not appear in the three-wire systems. With the introduction of the active filtering action, the three-phase current are sinusoidal and balanced, and consequently the current in neutral wire is practically zero. From the current spectra in Fig. 3, all the undesired harmonics are almost canceled except for the fundamental positive sequence. The individual harmonics distortions of the other sequence are all less than 1.2% before and after load change. Table 2 summarizes the detailed THD values of the currents.

The above results are obtained under the predefined switching frequency of 12.5 KHz as predefined switching frequency. Figs. 4 and 5 present a comparison between fixed-frequency and free-frequency sliding mode control performances for two predefined values of the switching

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**Table 1. Main parameters of the simulated system**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Phase to neutral source voltage</td>
<td>230 Vrms, 50 Hz</td>
</tr>
<tr>
<td>Source inductance</td>
<td>( L_r = 100 \mu F )</td>
</tr>
<tr>
<td>DC-bus capacitors</td>
<td>( C_1 = C_2 = 5 \mu F )</td>
</tr>
<tr>
<td>DC-bus voltage reference</td>
<td>( V_d^* = 100 \text{ V} )</td>
</tr>
<tr>
<td>Inductor</td>
<td>( L_e = 2 \text{ mH} )</td>
</tr>
<tr>
<td>Switching frequency reference</td>
<td>( f_s^* = 12.5 \text{ KHz} )</td>
</tr>
</tbody>
</table>

---

**Fig. 2. Complete block diagram of the proposed control**

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**Fig. 3.** Three-phase and neutral currents with and without active filtering.
(a) Three-phase and neutral currents without active filtering

(b) Three-phase and neutral currents with active filtering

(c) Current spectra for each phase without active filtering before and after load change

(d) Current spectra for each phase without active filtering before and after load change

Fig. 3. Three-phase currents, neutral currents and current spectra

(a) Instantaneous switching frequency with ± 3 A fixed hysteresis band

(b) Instantaneous switching frequency with ± 2.5 A fixed hysteresis band

(c) Instantaneous hysteresis bandwidth for 12.5 KHz predefined switching frequency

(d) Instantaneous hysteresis bandwidth for 20 KHz predefined switching frequency

(e) Instantaneous switching frequency for 12.5 KHz predefined frequency

(f) Instantaneous switching frequency for 20 KHz predefined frequency

Fig. 4. Switching frequency with free- and fixed-frequency sliding mode control
frequency. The switching frequencies shown in Figs. 4(a) and 4(b) result from a sliding mode control under free-frequency using hysteresis comparators with fixed-bandwidth. The bandwidths are chosen to obtain switching frequency turnaround values of 12.5 KHz and 20 KHz. Thus, the hysteresis bandwidths for Fig. 4(a) and Fig. 4(b) are fixed at ± 3 A and ± 2.5 A, respectively. The frequencies produce large oscillations (approximately 60% of the average value). For the proposed fixed-frequency, the instantaneous hysteresis bandwidths computed with respect to the previous frequencies and the instantaneous equivalent control are illustrated in Fig. 4(c) and Fig. 4(d), respectively. These bandwidths are not constant; however the instantaneous switching frequencies resulting from these bandwidths are almost constant as shown in Figs. 4(e) and 4(f). Under the same conditions, the source current in the a-phase using free-frequency and fixed-frequency control is illustrated in Fig. 5.

The total harmonic distortions are recapitulated in Table 3 which shows that the free-frequency control presents a slight superiority in terms of current tracking. However, no significant difference emerged. The THDs obtained with the two methods are strongly within the standard limits.

**Table 3. a-phase current THDs with free-frequency and fixed-frequency sliding mode control**

<table>
<thead>
<tr>
<th>Bandwidth</th>
<th>0.2 ts &lt; t &lt; 0.2 ts</th>
<th>0.2 ts &gt; t &gt; 0.2 ts</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fixed-bandwidth</td>
<td>1.27</td>
<td>2.27</td>
</tr>
<tr>
<td>Fixed-frequency</td>
<td>1.70</td>
<td>2.53</td>
</tr>
<tr>
<td>Fixed-bandwidth</td>
<td>1.23</td>
<td>2.35</td>
</tr>
<tr>
<td>Fixed-frequency</td>
<td>1.45</td>
<td>2.33</td>
</tr>
</tbody>
</table>

**6. Conclusion**

In the present paper, a sliding mode control under fixed switching frequency of three-phase three-leg voltage source inverter based four-wire shunt active filter using hysteresis comparators to generate the switching signals is achieved. The system is designed to compensate distorted and unbalanced currents under non-ideal main voltages. A detailed theoretical analysis of sliding mode and the problem of the switching frequency limitation is illustrated in a simple manner. The adopted solution which uses a variable hysteresis band for the switching signals generation has been verified through computer simulation. Very satisfactory results have been obtained which verified that the proposed fixed frequency control can conserve the excellent quality of the free frequency control in terms of current THD improvement by maintaining the switching frequency to a nearly constant value. Therefore, the present control system can be applied successfully to medium or high power electronics converters, such as multilevel structure-based compensators in power system, where a fixed switching frequency is often recommended because of the high power exchange.

**References**


Farid Hamoudi, A. Aziz Chaghi, Hocine Amineur and El Kheir Merabet

2003.


Farid Hamoudi received his engineering degree from the University of Bejaia, Algeria and the Master’s degree from the University of Batna, Algeria in 2005 and 2008, respectively. Currently, he is a PhD student at the University of Batna. His research interests include modeling and control of power electronic-based systems for power quality improvement in electric distribution systems. Since December 2009, he has served as assistant professor at the Institute of Electrical and Electronic Engineering at the University of Boumerdès, Algeria.