Feeder Re-assign Problem in a Surface Mount Device with a Piano-Type Multi-Headed Gantry

Hyunchul Tae, Byung-In Kim*
Department of Industrial and Management Engineering, Pohang University of Science and Technology (POSTECH), Pohang, Korea

(Received: January 28, 2013 / Revised: September 16, 2013 / Accepted: November 14, 2013)

ABSTRACT
A surface mount device (SMD) assembles electronic components on printed circuit boards (PCB). Since a component assembly process is a bottleneck process in a PCB assembly line, making an efficient SMD plan is critical in increasing the PCB assembly line productivity. Feeder assignment is an important part of the SMD plan optimization. In this paper, we propose a feeder re-assign improvement algorithm for a specific type of SMD machine with a piano type multi-head gantry. Computational results on some real-world benchmark data sets show the effectiveness of our proposed algorithm.

Keywords: Surface Mount Device, PCB Assembly, Feeder Assignment, Optimization, Improvement

* Corresponding Author, E-mail: bkim@postech.ac.kr

1. INTRODUCTION
Surface mount technology (SMT) is a printed circuit board (PCB) assembly technology used in many electronics manufacturing factories. A typical SMT assembly line has three main processes: solder paste, components placement (assembly), and re-flow process. The components placement process is conducted by a set of surface mount device (SMD) machines which place the components on PCB. Time studies and simulation results on SMT factories consistently show that the components placement process is a bottleneck process, thus increasing the productivity of components placement process is critical in increasing the productivity of the SMT assembly line. Increasing the number of SMD machines is a direct way of increasing the productivity of components placement process, but the expensive unit price of the SMD machine makes it difficult for factory owners to buy new SMD machines (Tirpak, 1993). Instead, developing an efficient algorithm for the SMD optimization problem is an easier option for factory owners to increase the productivity of components placement process.

The processing time of the components placement process can be divided into pick-up, place and nozzle change. In several previous papers that attempted to minimize the processing time, some authors (Sun et al., 2005; Wilhelm et al., 2006) focus on minimizing pick-up time, others (Duman and Or, 2004; Leipala and Nevalainen, 1989) focus on place time, and still others (Ayob and Kendall, 2004, 2005) focus on nozzle change time. In this paper, we propose a feeder re-assign improvement algorithm to minimize the pick-up time.

2. SMD MACHINE DESCRIPTION
The nature of the SMD optimization problem is highly dependent on the characteristic of target SMD machine because the problem’s formulation, size, and complexity are determined by the SMD machine’s characteristic (Duman and Or, 2004). Thus, a tailored algorithm is required for each specific SMD machine type. In the current SMD market, there are many different types of SMD machines such as the single gantry system or dual gantry system, piano type or turret type and multi-headed or single-headed (Ayob and Kendall, 2008). This paper considers a specific SMD machine with a piano-type multi-headed single-gantry. Its schematic view is described in Figure 1.
3. SMD OPTIMIZATION PROBLEM

A PCB has placement points \( P = \{p_1, p_2, \ldots, p_n\} \) on its surface, and each placement point has its own \( x \)-\( y \) coordinates and component type \( c_i \in C = \{c_1, c_2, \ldots, c_m\} \). A component of type \( c_i \) is provided by a feeder \( fd_i \), and there exists at least one feeder \( fd_i \) for type \( c_i \). Note that in our case, there could be more than one feeder \( fd_i \) for type \( c_i \). Every placement point on PCB should be placed with the proper component by the SMD machine. Since the SMD machine’s gantry has six heads, up to six components can be picked and placed by a single trip of the gantry. We call the trip “cycle”, denoted by \( l \) and a series of such cycles that fulfills the placement requirement completely a “SMD plan”, denoted by \( L = \{l_1, \ldots, l_l\} \).

A cycle consists of three sequential steps of a gantry: nozzle change, components pick-up and components placement steps. The cycle time is the time required to complete the three steps of the cycle and equals to the sum of nozzle change time, components pick-up time, and components placement time. Nozzle change time is the time required to change the nozzles of the heads of a gantry. The nozzle change step can be skipped if nozzle change is not required for the cycle. Pick-up time means the required time of a cycle to pick components from feeders. The placement time is the required time of a cycle to place components onto the placements. The SMD plan time equals to the sum of cycle times of all the cycles in the SMD plan.

An SMD optimization problem is the problem of finding the SMD plan and its corresponding feeder assignment with the minimum SMD plan cycle time. Since the SMD optimization problem is a hard combinatorial problem, many researchers have solved it by dividing it into sub-problems (Lee et al., 2000; Van Laarhoven and Zijm, 1993; Wilhelm and Tarmy, 2003). The SMD optimization problem can be divided into two major sub-problems: a feeder assigning problem and a cycle generation problem. The two sub-problems are highly related so one should solve the two sub-problems together to get the optimal solution of the SMD optimization problem.

However, solving the two sub-problems together have never been attempted because the SMD optimization problem and sub-problems are hard combinatorial optimization problems, and taking the problems together is too difficult (Van Laarhoven and Zijm, 1993). Instead, sequential approaches have been used to solve the SMD optimization problems (Lee et al., 2000; Van Laarhoven and Zijm, 1993). A sequential approach is a method to solve a complicated problem by solving its sub-problems sequentially. For example, a solution for SMD optimization problem can be obtained by solving the feeder assignment problem first and then the cycle generation problem.

The sequential approach promises the reasonable computational cost but its solution may be a local optimum. To overcome the local optimality, Grunow et al.
(2004) adopted an improving method to the sequential approach. We solved the SMD optimization problem with the following consequent procedures as Grunow et al. (2004) did: 1) an initial feeder assignment procedure, 2) a cycle construction procedure, and 3) an improvement procedure. The initial feeder procedure aims to find feasible feeder assignment so that every placement can be placed. In this procedure, only one feeder for one component is allowed to be assigned into a feeder base even though the component has more than one feeder. With the assumption that every head can reach every placement and feeder slot’s pick-up point, this rule (one feeder for one component) does not cause infeasibility.

The cycle construction procedure makes a series of cycles or a SMT plan based on initially assigned feeders with the minimum possible SMT plan time. This procedure constitutes four sub-problems: a placement point assignment problem, a feeder selection problem, a pick-up sequencing problem, and a placement sequencing problem. The improvement procedure tries to improve the SMD plan with two procedures: a placement exchange procedure and a feeder re-assigning procedure. The placement exchange procedure tries to change placements within a cycle or between the cycles the SMD plan, and the feeder re-assigning procedure tries to change feeder positions or to assign duplicate feeders.

Although this paper focuses on the feeder re-assignment problem, we explain the sub-problems of the cycle construction procedure since they are highly interrelated with the feeder re-assignment procedure. The placement point assignment problem assigns all placement points to a 6-column matrix (if a gantry has 6 heads) as an example is shown in Figure 3. In Figure 3, there are three matrixes, and each matrix denotes a SMT plan. Each plan has rows, and each row means a cycle, and the order of rows is the order of cycles in descending direction. Each column corresponds to a head, and the index of a column means the index of head.

In Figure 3, there are three plans \{L_1, L_2, L_3\} for two placement points \(P = \{p_1, p_2\}\). The difference between plan \(L_1 = \{l_1\}\) and \(L_2 = \{l_2, l_3\}\) is that \(L_1\) places \(P\) with one cycle \(\{l_1\}\) while \(L_2\) does with two cycles \(\{l_2, l_3\}\). Generally, \(L_1\) is more efficient than \(L_2\). \(L_3 = \{l_4, l_5, l_6\}\) is infeasible plan because of its second row \(l_2\) has no placement point.

![Figure 3. Placement point assignment problem example.](image)

![Figure 4. Extended row with feeder selection, pick-up sequence and place sequence.](image)

The feeder selection problem selects a feeder for each placement point in the matrix. If a feeder base has only one feeder for one component, then this problem is trivial. But in reality, there might be several feeders for a component type, and in those cases, it is important to decide which feeder should be selected. The feeders in the upper right corner of the cells show the selected feeders in Figure 4.

The pick-up sequencing problem decides the pick-up order of components from the selected feeders, and the placement sequencing problem decides the order of components placing onto the placement points. Both problems can be viewed as a travelling salesman problem with at most six stops, and the problems could be solved easily since they are small size problems. Note that our target SMD machine automatically decides both sequences. The numbers in the bottom left and bottom right corners of the cells in Figure 4 show the pick-up sequence and the placement sequence within a cycle, respectively.

### 4. PROPOSED ALGORITHM

This paper aims to propose a feeder re-assignment algorithm. The feeder re-assignment algorithm of Grunow et al. (2004) tries to swap two feeders, and if the swap brings lower SMT plan time, it is accepted. They reported significant time reduction in SMT plan time by their simple 2-opt based improving algorithm. However, their approach has some limitations to be applied for our problem as explained below, so we propose a new feeder re-assignment algorithm.

- **Gang pick:** Our target SMD machine can pick components simultaneously from a set of feeders while this is not allowed in Grunow et al. (2004). Sun et al. (2005) stated that the gang pick is critical in minimizing pick-up time. The gang pick requires specific condition to happen. Feeders need to be reassigned in proper distance in order to increase the number of gang picks.

- **Duplicate feeder:** In the initial assignment procedure, only one feeder for one component is allowed to be assigned to feeder base as in Grunow et al. (2004). However, in our case, more than one feeder can be assigned if the additional feeders can reduce the pick-up time. Assigning duplicate feeders may decrease the gantry’s moving distance on the feeder base and increase the possibility of gang pick.

A feeder assignment can be denoted by \((s, F)\), where \(s\) is feeder start index or the lowest index of the
feeder slot which has a feeder and \( F \) is a feeder arrangement. Feeder assignment in Figure 5 would be described as \( (25, \{fd_1, fd_2, fd_0, fd_3, fd_4, fd_5, fd_6, fd_8, fd_9\}) \), where \( fd_0 \) means a feeder of component \( k \) or \( c_0 (k > 0) \) and \( fd_6 \) means an empty feeder. A SMT plan \( L = \{l_1, l_2, \ldots, l_n\} \) is a series of cycles, where \( l_i \) means \( i \)-th cycle of plan \( L \). Each cycle \( l_i = (l_{i0}, l_{i1}, l_{i2}, l_{i3}, l_{i4}) \) is a set of head operations, where \( l_{ij} \) denotes an operation of \( j \)-th head in cycle \( l_i \). Each head operation \( l_{ij} \) constitutes with four parts, such as chosen placement, chosen feeder, determined placement sequence, and pick-up sequence as shown in Figure 4. Our proposed algorithm is an improving algorithm so that an initial solution \( x_0 = (s_0, F_0, L_0) \) is assumed to be given where \( (s_0, F_0) \) is the initial feeder assignment and \( L_0 \) is the initial SMD plan.

The initial SMD plan \( L_0 \) is the best plan under the feeder assignment \( (s_0, F_0) \). If we change feeder assignment from \( (s_0, F_0) \) to \( (s^*, F^*) \), then we need to change the initial plan \( L_0 \) to \( L^* \), as fitted to \( (s^*, F^*) \). Procedure feederSelectOpt does this job, it changes a plan \( L \) to be fitted to a feeder assignment \( (s, F) \) considering feeder selection, pick-up and placement sequencing. Note that placement points are not re-assigned here even though it promises better result because solving placement point assignment problem requires expensive computational cost. Function \( feederSelectOpt(s_0, F_0, l_0) \) returns a set of feeder slot indices \( I = \{3, 6, \cdots\} \) which have feeders with the same component type with a placement in \( l_0 \). If \( l_{ij} \) has no placement then it returns \( \emptyset \). Function \( setPick\&PlaceSeq(l, s, F) \) optimally sets pick and placement sequence of head operations in cycle \( l_i \) based on the feeder arrangement \( (s, F) \). Function \( T(l, s, F) \) returns the time of cycle \( l_i \) based on \( (s, F) \) and \( T(l, s, F) \) returns the \( L^* \) SMD plan time based on \( (s, F) \).

```plaintext
procedure feederSelectOpt(l_0, s, F)
for i = 1 to |C|  //|C| is the number of cycles in C
for j = 1 to 6
l = feederSelectOpt(l_0)
for k = 1 to |I|
l
l
let \( l_{ij}^\ast \) use feeder in feeder slot \( l_i \); setPick\&PlaceSeq(\( l_i^\ast \), s, F);
if \( T(l_i^\ast, s, F) < T(l_i, s, F) \)
\( l_i = l_i^\ast \);
```

Our proposed algorithm \( feederReAssignmentAlgorithm \) receives an initial solution \( (L_0, s_0, F_0) \) and returns improved solution \( (L, s, F) \) with decreased SMT plan time. The proposed algorithm uses four sub-algorithms: feeder2Opt, emptyFeederInsertion, moveFeeders, feederAddition.

```plaintext
procedure feederReAssignmentAlgorithm(L_0, s_0, F_0)
\( (L, s, F) = feederAddition(L_0, s_0, F_0) \);
\( \{/decrease feeder addition\}
\( (C, s, F) = feeder2Opt(C_0, s_0, F_0) \);
Repeat  // Empty Feeder Insertion
\( (C^*, s^*, F^*) = emptyFeederInsertion(C^*, s^*, F^*) \);
\( (C^*, s^*, F^*) = moveFeeders(C^*, s^*, F^*) \);
if \( T(C^*, s^*, F^*) < T(C, s^*, F^*) \)
\( (C, s, F) = (C^*, s^*, F^*) \);
until \( \exists \)
\( return(L, s, F) \);
```

Procedure feeder2Opt is a simple 2-opt based algorithm which tries to change two different feeders, and if the change results in a lower plan time, it accepts the change. Note that changing empty feeder with non-empty feeder is possible. \( N_F(F) \) denotes a set of feeder arrangement candidates by swapping two different feeders of \( F \). The function \( feasiFd(s, F) \) return \( \text{true} \) if feeder assignment \( (s, F) \) is possible or \( \text{false} \) otherwise.

```plaintext
procedure feeder2Opt(L, s, F)
for each \( F^* \in N_F(F) \)
if \( \text{feasiFd}(s, F^*) \) is true
\( L^* = L \);
feederSelectOpt(L^*, s, F^*);
if \( T(L^*, s, F^*) < T(L, s, F) \)
\( F = F^*; \)
\( L = L^*; \)
Restart for each loop;
return(L, s, F);
```

Procedure emptyFeederInsertion tries to insert an empty feeder into the feeder arrangement to increase the possibility of gang picks. For example, if two 8-mm feeders are arranged right next to each other, then two feeders cannot be picked simultaneously because the feeders’ distance between pick-up points (8 mm) is smaller than the distance between heads (16 mm). However, by inserting an empty feeder between the feeders, the feeders can be picked simultaneously. Function \( insertFd(fd, F, i) \) inserts the feeder \( fd \) into \( i \)-th position of \( F \) and returns the inserted feeder arrangement.

```plaintext
procedure emptyFeederInsertion(L, s, F)
\( L^* = L \);
\( F^* = F \);
for i = 1 to |F|  //|F| is the number of feeders in F
\( F^* = insertFd(fd_0, F, i) \);
if \( \text{feasiFd}(s, F^*) \) is true
\( L^* = L \);
feederSelectOpt(L^*, s, F^*);
if \( T(L^*, s, F^*) > T(L, s, F^*) \)
\( L^* = L^*; \)
\( F^* = F^*; \)
return(L^*, s, F^*);
```

Procedure moveFeeders attempts to find the best start point of the first feeder of the feeder arrangement.
Let what kind of feeder to be added and where it is added. Not all feeders can be assigned. It is important to decide time. However, due to the limited number of feeder slots, der into the feeder base always promises less pick-up feeder into the feeder base. Assigning a duplicate fe-

cord placements in descending order and coordinate of placements with component type 

tion coordinate of 

feeder infeasibility.

feeder can be assigned then it returns -1 as a sign of

infeasibility.

Procedure feederAddition tries to assign duplicate feeders into the feeder base. Assigning a duplicate feeder into the feeder base always promises less pick-up time. However, due to the limited number of feeder slots, not all feeders can be assigned. It is important to decide what kind of feeder to be added and where it is added. Let \( C_{sort} \) be a sorted component types by the number of placements in descending order and \( C_{sort,i} \) be a component which has \( i \)-th number of placements. Let \( x\text{avecord}(c_i) \) be a function which returns the average \( x \)-coordinate of placements with component type \( c_i \). Function idealIdx(\( x, fd \)) returns a feeder slot index to which feeder \( fd \) can be assigned and which is the closest to the coordinate of \( x \). If there is no feeder slot to which the feeder can be assigned then it returns -1 as a sign of infeasibility.

**procedure** moveFeeders \((L, s, F)\)

\[
L^* = L; \ s^* = s;
\]

for \( s^* = 0 \) to 60

if feasFd \((s^*, F)\) is true

\[ L' = L; \]

feederSelectOpt \((L', s^*, F); \)

if \( T(L^*, s^*, F) > T(L, s^*, F) \)

\[ L^* = L'; \ s^* = s'; \]

return \((L^*, s^*, F)\);

Procedure feederAddition tries to assign duplicate feeders into the feeder base. Assigning a duplicate feeder into the feeder base always promises less pick-up time. However, due to the limited number of feeder slots, not all feeders can be assigned. It is important to decide what kind of feeder to be added and where it is added. Let \( C_{sort} \) be a sorted component types by the number of placements in descending order and \( C_{sort,i} \) be a component which has \( i \)-th number of placements. Let \( x\text{avecord}(c_i) \) be a function which returns the average \( x \)-coordinate of placements with component type \( c_i \). Function idealIdx(\( x, fd_i \)) returns a feeder slot index to which feeder \( fd_i \) can be assigned and which is the closest to the coordinate of \( x \). If there is no feeder slot to which the feeder can be assigned then it returns -1 as a sign of infeasibility.

**procedure** feederAddition\((L, s, F)\)

\[
C_{sort} = \text{a sorted } C \text{ with the placement number }
\]

Repeat

for \( i = 0 \) to \( |C_{sort}| \)

if there remains feeder of \( C_{sort,i} \)

\( x\text{cord} = x\text{avecord}(c_i); \)

\( \text{idx} = \text{idealIdx}(x\text{cord}, fd_i); \)

if \( \text{idx} \) is not -1

\( F' = \text{insertFd}(fd_{d0}, L); \)

if feasFd \((s, F')\) is true

\[ L' = L; \]

until impossible to add a feeder

**5. COMPUTATIONAL RESULTS AND CONCLUSION**

Table 1 shows computational results of the proposed feeder re-assignment algorithm. The proposed algorithm was programmed in C++ language, and the computational test was performed on an Intel Dual Core E7300 2.66 GHZ 2.67 GHZ/4 GB RAM PC. Initial SMD plan \((C_0, s_0, F_0)\) is provided by the given construction algorithm of Tae and Kim (2013). The benchmark data were obtained from an anonymous SMD company in Korea. The computational results show that our proposed algorithm could improve the initial SMT plan by 1.68% on average.

In this paper, we described an SMD optimization problem for a piano-type multi-head SMD machine. We briefly introduced its specific characteristics of the SMD, such as gang-pick. We proposed a feeder re-assign improvement algorithm based on feeder-move and feeder-change. Our Computational results show the effectiveness of our proposed algorithm on some real-world benchmark data set.

**REFERENCES**


Ayob, M. and Kendall, G. (2005), An on-line constructive heuristic to optimise the hybrid pick and place machine, School of CSIT, The University Of Nottingham, Nottingham, UK.

**Table 1. Computational result of proposed algorithm**

<table>
<thead>
<tr>
<th>Benchmark data</th>
<th>No. of components</th>
<th>No. of placements</th>
<th>No. of tape feeders</th>
<th>No. of tray feeders</th>
<th>No. of cycles</th>
<th>SMT plan time before (sec)</th>
<th>SMT plan time after (sec)</th>
<th>Reduced time (sec)</th>
<th>Reduced (%)</th>
<th>Computation time (sec)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3_01</td>
<td>7</td>
<td>14</td>
<td>4</td>
<td>1</td>
<td>8</td>
<td>20.34</td>
<td>20.04</td>
<td>0.29</td>
<td>1.4</td>
<td>0.02</td>
</tr>
<tr>
<td>3_02</td>
<td>11</td>
<td>22</td>
<td>8</td>
<td>1</td>
<td>12</td>
<td>28.66</td>
<td>28.37</td>
<td>0.28</td>
<td>1.0</td>
<td>0.09</td>
</tr>
<tr>
<td>3_03</td>
<td>11</td>
<td>47</td>
<td>16</td>
<td>1</td>
<td>21</td>
<td>57.57</td>
<td>56.67</td>
<td>1.73</td>
<td>1.5</td>
<td>0.95</td>
</tr>
<tr>
<td>3_04</td>
<td>11</td>
<td>47</td>
<td>8</td>
<td>1</td>
<td>26</td>
<td>59.30</td>
<td>58.30</td>
<td>0.99</td>
<td>1.7</td>
<td>0.28</td>
</tr>
<tr>
<td>3_05</td>
<td>11</td>
<td>47</td>
<td>16</td>
<td>1</td>
<td>26</td>
<td>58.92</td>
<td>57.55</td>
<td>1.34</td>
<td>2.3</td>
<td>2.14</td>
</tr>
<tr>
<td>3_06</td>
<td>11</td>
<td>47</td>
<td>11</td>
<td>1</td>
<td>21</td>
<td>56.65</td>
<td>55.76</td>
<td>0.89</td>
<td>1.6</td>
<td>0.66</td>
</tr>
<tr>
<td>3_07</td>
<td>12</td>
<td>50</td>
<td>11</td>
<td>1</td>
<td>23</td>
<td>62.64</td>
<td>61.47</td>
<td>1.19</td>
<td>1.9</td>
<td>0.67</td>
</tr>
<tr>
<td>3_08</td>
<td>13</td>
<td>52</td>
<td>11</td>
<td>1</td>
<td>27</td>
<td>68.06</td>
<td>67.69</td>
<td>0.38</td>
<td>0.5</td>
<td>0.53</td>
</tr>
<tr>
<td>Average</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>0.89</td>
<td>1.5</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>


