Digital Control of an AC/DC Converter using the Power Balance Control Technique with Average Output Voltage Measurement

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Abstract

This paper presents a method for the digital control of a high power factor AC/DC converter employing the power balance control technique to achieve a fast response of the output voltage control. To avoid the effects of an output voltage ripple in the voltage control loop, the average output voltage is sampled and used as a feedback signal for the output voltage controller. The proposed control technique was verified by simulations using MATLAB/Simulink and its implementation was realized by a dsPIC30F4011 digital signal processor to control a CUK topology AC/DC converter with a 48 V output voltage and a 250 W output power. The experimental results agree with the simulation results. The proposed control technique achieves a fast transient response with a lower line current distortion than is achieved when using a conventional proportional-integral controller and the power balance control technique with the conventional sampling method.

Key Words: AC/DC converter, Digital Control, Power factor correction

I. INTRODUCTION

The switched mode dc power supply is an important piece of equipment that provides a dc power source for an electronic circuit in many systems such as in computers, telecommunications, medical equipment and industrial control systems. Normally the input of a switching mode power supply consists of a diode bridge rectifier for converting ac voltage to dc voltage which is then used as the input of the DC/DC converter. The output voltage of the DC/DC converter is regulated to a desired voltage level. To avoid the harmonic currents that are generated by a simple diode bridge rectifier a power factor correction (PFC) circuit is used instead of the diode bridge rectifier. PFC circuits can be divided into passive and active PFC circuits. The passive type is usually used in a low power applications due to the penalties of weight and size of the passive components that are used in the circuits which must have a high value to comply with the harmonics current regulations. The active type PFC circuit is derived from the conventional switching mode DC/DC converter. Converter topology selection is based on the application requirements such as the voltage conversion ratio, the isolation between the utility and the load, and the difficulty in smoothing the input current. The PFC circuits in [1]–[7] operate in both the continuous conduction mode (CCM) and in the discontinuous conduction mode (DCM). Moreover boundary conduction mode (BCM) PFC circuits have been presented in [8]–[10]. The main advantage of the CCM PFC circuit is its lower peak input current when compared to the DCM and BCM PFC circuits at the same power level. However in the DCM and BCM PFC circuits, only an output voltage control loop is required. For the CCM PFC circuit the control loops are the current input control and the output voltage control as an inner loop and an outer loop, respectively. The response of the current control loop for the CCM PFC circuit should be fast enough to force the input current to follow the input ac voltage waveform for high power factor purposes. To avoid line current distortion in the CCM PFC circuit, the system crossover frequency in the voltage loop should be between 10–20 Hz (50 Hz line frequency) [11]. According to the low crossover frequency, the transient response of the PFC circuit is limited especially under the step-load condition. Methods to improve the dynamic response of the PFC circuit have been proposed. Output voltage ripple cancellation has been presented in [12]–[15], for eliminating the voltage ripple from the error signal of the voltage loop and for improving the output voltage control response. However the filter design is complex and the implementation requires a fast digital controller such as a high speed digital signal processor (DSP) or a field programmable gate array (FPGA) platform. A simple control technique has been proposed in [16]. This method is based on load current feedforward and the concept of input-output power balance. However, with this
method, a voltage ripple occurs in the voltage control loop and it causes a distortion in the input current waveform. This paper presents a control method for an AC/DC converter. The input-output power balance concept and the average output voltage sampling method are used as control strategies. The proposed sampling method is used to eliminate the effects of the voltage ripple in the control loop. A load current transducer is used in the proposed method. Moreover it can be used for active current sharing purposes when multiple converters are connected in parallel for a high power applications. The proposed control method is demonstrated on a 250 W CUK AC/DC converter and the control algorithm is implemented with a low cost DSP (dsPIC30F4011 from Microchip). The advantages of the digital control system over an analog control implementation include a lower sensitivity to environmental variations, ease of changing the control algorithm by updating the control firmware in the programmable memory of the processor, and the fact that the system can communicate with other parallel connected modules by using the built in serial communication. Load current sharing and redundancy operation in the modular approach AC/DC converter can be performed.

II. THE PROPOSED SYSTEM AND SAMPLING METHOD

Fig. 1 shows the proposed system including the control algorithm. The CUK topology AC/DC converter has inductors at the both ends of the circuit. As a result, the input and output current of the converter can be continuous, and the input current is easily controlled to be sinusoidal as the input line voltage. The CUK AC/DC converter is designed to operate in the CCM mode and the input inductor current is controlled by a simple analog circuit which is a hysteresis current controller. A zero crossing detector (ZCD) circuit is used to detect the zero crossing point (0°) of the input ac line voltage. The output signal of the ZCD circuit is used to interrupt the dsPIC30F4011 DSP by feeding this signal to the INT0 pin. When the output signal of the ZCD circuit is changed from 0 V (logic ‘0’) to 5 V (logic ‘1’), an INT0 interrupt occurs. After that the interrupt service routine (ISR) for the INT0 interrupt is active and the variable in the software counter block is cleared to zero. The value of the software counter is increased by 1 every 200μs. A time base with a 200μs time interval is generated by the internal timer of the dsPIC30F4011 and this time base is also used as a sampling period for the digital controller that is used to regulate the output voltage of the converter. The A/D blocks in Fig. 1 are the internal analog to digital converters of the dsPIC30F4011 and the resolution is 10 bit with a 5 V maximum input voltage. Three internal analog to digital converters are used in the proposed system. The D/A is a digital to analog converter circuit that is used to convert the digital control signal to an analog signal. The analog signal from the D/A is a command which is used to control the input inductor current $i_{L1}$ of the CUK converter. From the input-output power balance concept, if the input line current $i_L$ is controlled to be sinusoidal and in phase with the line voltage, the input and the output power equations of the circuit can be written as in (1)-(2).

$$P_{out}(t) = \eta \cdot p_{in}(t) \quad (1)$$

$$P_{out}(t) = \eta \cdot \bar{V}_g \sin(\omega t) \cdot \bar{I}_s \sin(\omega t) \quad (2)$$

where $\bar{V}_g, \bar{I}_s$ and $\eta$ are the peak value of the line voltage, the peak value of the input current and the efficiency of the converter, respectively. Rearrange (2) and the output power can be written as shown in (3).

$$P_{out}(t) = \eta \cdot V_g \cdot I_L(1 - \cos(2\omega t)) \quad (3)$$

where $V_g$ and $I_L$ are the rms value of the input ac line voltage and the input current of the AC/DC converter. From (3) the output power consists of two terms but the average output power is equal to the dc term of (3). The inductor current $i_{L2}$ can be calculated by dividing (3) by the average output voltage $\bar{V}_o$. Therefore, the output inductor current can be written as in (4) and the ac component of this current is the capacitor current $i_{C_o}(t)$ in (5).

$$i_{L2}(t) = \eta \cdot \frac{V_g \cdot I_L}{V_o} (1 - \cos(2\omega t)) = I_o - \eta \cdot \frac{V_g \cdot I_L}{V_o} \cos(2\omega t) \quad (4)$$

$$i_{C_o}(t) = \frac{\eta \cdot V_g \cdot I_L}{V_o} \cos(2\omega t). \quad (5)$$

From the output capacitor current, the instantaneous value of the output voltage can be obtained by integrating the output capacitor current in (5). The output voltage ripple can be written as in (6)-(7).

$$v_{ripple}(t) = \frac{1}{C_o} \int \frac{\eta \cdot V_g \cdot I_L \cos(2\omega t)}{V_o} dt \quad (6)$$

$$v_{ripple}(t) = \frac{\eta \cdot V_g \cdot I_L}{2 \cdot \omega \cdot C_o \cdot V_o} \sin(2\omega t + \pi) \quad (7)$$

where $v_{ripple}$ is the output voltage ripple of the converter and $\bar{V}_o$ is the average value of the output voltage. From the output voltage ripple in (7), it can be seen that the voltage ripple runs at twice the line frequency and that the magnitude of the voltage ripple is zero at the zero crossing point of the line voltage. Thus the average output voltage can be measured by reading the output voltage at the zero crossing point of
the input line voltage of the converter. From the relation between the output voltage ripple waveform and the input line voltage waveform, the output voltage should be sampled by the internal analog to digital converter of the DSP at the zero crossing point of the ac line voltage waveform. Therefore a timing diagram of the output voltage sampling process can be drawn as shown in Fig. 2. The variable \( v_{O, \text{sync}} \), in Fig. 1, is obtained by updating the output voltage sensed at the zero crossing point of the ac line voltage. The variable\( v_{O, \text{ref}} \) is the output voltage command.

Fig. 3 shows the converter power circuit. A high frequency transformer is inserted in a conventional CUK converter to isolate the load and the ac voltage source for safety and voltage scaling reasons. The parameters of the circuit are determined by the calculation method presented in [17]. The parameters of the power circuit used in this paper are shown in Table I.

### III. Transfer Function and Controller Design

The transfer function of a power converter should be determined prior designing and calculating the controller parameters. By using the average small signal analysis to find a transfer function of the power converter, the dynamic equation at the output node of the power circuit in Fig. 3 can be written as in (8).

\[
i_{L2} = C_O \frac{dv_O}{dt} + i_O. \tag{8}
\]

Using the input and output power balance concept, the relationship between both of the inductor currents can be written as in (9).

\[
i_{L2} \cdot v_O = \frac{i_{L1} \cdot V_g}{\sqrt{2}}. \tag{9}
\]

### TABLE I

<table>
<thead>
<tr>
<th>Input Voltage</th>
<th>220 V / 50 Hz</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output Voltage</td>
<td>48 V</td>
</tr>
<tr>
<td>Power</td>
<td>250 W</td>
</tr>
<tr>
<td>( C_1 = C_2 )</td>
<td>0.68 ( \mu )F</td>
</tr>
<tr>
<td>( L_1, L_2 )</td>
<td>5 mH, 1 mH</td>
</tr>
<tr>
<td>( C_O )</td>
<td>25000 ( \mu )F</td>
</tr>
</tbody>
</table>

The control loop of the AC/DC converter employs a proportional-integral (PI) controller with the power balance control technique, as shown in Fig. 4. The magnitude of the input inductor current command is obtained by adding the control signal \( v_C \) of the output voltage controller and the current \( \hat{i}_{L1} \), which is calculated from the power balance concept. Therefore, the magnitude of the input inductor current command can be written as in (11).

\[
\hat{I}_{L1,\text{ref}} = \hat{i}_{L1} + v_C. \tag{11}
\]

A PI controller is chosen to control the output voltage, and the continuous-time PI controller transfer function is shown in (12).

\[
G_C(s) = K_P + \frac{K_I}{s}. \tag{12}
\]

where \( K_P \) is the proportional gain and \( K_I \) is the integral gain of the continuous-time PI controller. After applying a perturbation, the small signal approximation method is used on (8)-(11). Then the results are then shown in (13)-(16).

\[
\bar{v}_O = \frac{\bar{i}_{L2} - \bar{i}_{\text{Load}}}{C_O s}. \tag{13}
\]

\[
\bar{i}_{L2} = \frac{V_g}{\sqrt{2} \cdot \bar{V}_O} \cdot \hat{i}_{L1} + \frac{V_g}{\sqrt{2} \cdot \bar{V}_O} \cdot \hat{V}_g - \frac{\bar{I}_{L2}}{\bar{V}_O} \cdot \bar{V}_O. \tag{14}
\]

\[
\bar{i}_{L1} = \frac{\sqrt{2} \bar{I}_{\text{Load}}}{V_g} \cdot \bar{v}_O + \frac{\sqrt{2} \bar{V}_O}{V_g} \cdot \bar{i}_{\text{Load}} - \frac{\bar{I}_{L1}}{\bar{V}_O} \cdot \bar{V}_g. \tag{15}
\]

\[
\bar{\hat{i}}_{L1,\text{ref}} = \bar{\hat{i}}_{L1} + \bar{v}_C. \tag{16}
\]

From (13)-(16), the variables with the sign ‘\( \hat{\cdot} \)’ are the steady state values and the variables with the sign ‘\( \bar{\cdot} \)’ are the small signal values. From (13)-(16), a block diagram that shows the

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Fig. 2. Waveforms of line voltage, output voltage ripple, output of the zero crossing detector circuit and value of software counter.

Fig. 3. The power circuit of the CUK AC/DC converter.

Fig. 4. CUK AC/DC converter controlled by the PI controller with the power balance control technique.
relation between the output voltage and the output voltage command can be drawn. By using the block diagram reduction method, as in classical control theory, the transfer function of the output voltage to the output voltage command can be written as in (17).

\[
\frac{\tilde{v}_O}{\tilde{v}_{Oref}} = \frac{G_C(s) V_g}{\sqrt{2V_O C_O s + k_1 G_C(s) V_g}} \tag{17}
\]

where \(k_1\) is the feedback gain used to attenuate the voltage from the output node to an appropriate value for the internal analog to digital converter of the dsPIC30F4011. From the closed loop transfer function in (17), the open loop transfer function \(G_P(s)\) without the controller \(G_C(s)\) can be written as in (18).

\[
G_P(s) = \frac{k_1 V_g}{\sqrt{2V_O C_O s}} \tag{18}
\]

The controller parameters are chosen by using the frequency response design method. Before calculating the parameters, the frequency responses of the compensated system are specified such as the phase margin value and the crossover frequency. From (18), the phase value of the uncompensated system is \(\theta_R\). Then the phase value of the controller is obtained by (19).

\[
\theta_C = \frac{(90^\circ - \theta_R) \cdot \pi}{180} \tag{19}
\]

where \(\theta_C\) and \(\theta_R\) are the controller phase value and the required phase margin of the compensated system. From the controller phase value and the controller transfer function, the controller parameters can be obtained by (20)-(21).

\[
K_P = \frac{\cos(\theta_C)}{G_{PO}} \tag{20}
\]

\[
K_I = \frac{\sin(\theta_C) \cdot 2\pi f_C}{G_{PO}} \tag{21}
\]

where \(G_{PO}\) is the magnitude of the open loop transfer function \(G_P(s)\) at the crossover frequency \(f_C\). After the analog PI controller design is finished, the parameters are used to calculate the control signal of the dsPIC30F4011 DSP by (22).

\[
u(n) = K_P \cdot e(n) + K_I \cdot T_s \sum_{k=1}^{n} e(n) \tag{22}
\]

where \(u(n)\) is the output control signal of the digital controller, \(e(n)\) is an error signal and \(T_s\) is a sampling time period.

IV. EFFECT OF THE OUTPUT VOLTAGE RIPPLE IN PFC CONTROL

Regarding the two control loops of the AC/DC converter described earlier, the current controller will force the input current to track the current command. The current command is generated by multiplying an absolute sine wave signal, which is in-phase with the input line voltage, by a control signal from the output voltage controller. The concept of the two control loops of the PFC circuit is shown in the diagram in Fig. 5. The ideal case for the steady state condition is that, the control signal from the voltage loop controller should be a dc signal. Then the resultant signal after multiplication with an absolute sine wave will be an absolute sinusoidal signal with a low level of distortion. However, in practical realization, the control signal from the output voltage controller is not a pure dc signal. It also contains a ripple that is affected by the ripple in the output of the power converter. Then the inductor current command of the later case will be a distortion of an ideal sinusoidal wave, as shown in Fig. 6.

The distorted input current of the AC/DC converter is influenced by the voltage ripple in the voltage control loop as discussed in [8] and [17]. However, with the proposed sampling method in this paper, the influence of the output voltage ripple on the voltage control loop is minimized. Because the sampling is always read, the output voltage is at the point when its value is approximately equal to the average value of the output voltage. Then the input current distortion can be reduced.

V. SIMULATION RESULTS

The CUK AC/DC converter is simulated by the MATLAB/Simulink simulation program. The converter parameters for the simulation are shown in Table I. The results show the transient response of the output voltage control and the steady state performance of the proposed control method compared with converters that are controlled by a conventional PI controller and a PI controller with the power balance control technique. Fig. 7 and Fig. 8 show the transient response and the steady state response of the CUK AC/DC converter controlled by the digital PI controller with a 200 \(\mu\)s sampling time period for updating the output voltage value and for calculating the control signal. In Fig. 7, the load is changed from 200 W to 100 W and then back to 200 W at 0.4 s and 0.7 s, respectively. The settling time in Fig. 7 is 100 ms while the load changed from 200 W to 100 W with a 2.72% overshoot. Fig. 8 shows

![Fig. 5. The Control loop of the AC/DC converter.](image)

![Fig. 6. The distortion of the inductor current command with the ripple voltage on a signal from the voltage controller.](image)
Fig. 7. Simulated transient response of the CUK AC/DC converter controlled by the PI controller with 200\(\mu\)s sampling time.

Fig. 8. Simulated steady state response of the CUK AC/DC converter controlled by the PI controller with 200\(\mu\)s sampling time.

Fig. 9. The harmonics spectrum of the input current in Fig. 8.

Fig. 10. Simulated transient response of the CUK AC/DC converter controlled by the PI controller with the power balance control technique using 200\(\mu\)s sampling time.

Fig. 11. Simulated steady state input current and voltage of the converter controlled by the PI controller with the power balance control technique and 200\(\mu\)s sampling time period.

the input current of the PFC circuit at 200W of output power that is in phase with the input voltage. However, the current distortion still occurred and can be seen in the current spectra as shown in Fig. 9.

The results in Fig. 10 and Fig. 11 show the transient response and the steady state response of the CUK AC/DC converter that is controlled by using the PI controller with the power balance control technique, as shown in Fig. 4. However, the results in Fig. 10 and Fig. 11 are for the conventional sampling method with a 200\(\mu\)s sampling time for the controller. The results in Fig. 10 and Fig. 11 show that the load condition is still using a step load from 200 W to 100 W and then back to 200 W at 0.4 s and 0.7 s, respectively. In Fig. 10 the output voltage is almost constant even when the load power is changed from 200 W to 100 W at 0.4 s. From the results, it can be seen that the voltage ripple can be increased by increasing the load current. Fig. 11 shows the steady state response of the input current and input voltage of the AC/DC converter with a 200 W load.

Fig. 12 is the input current harmonics spectrum of the AC/DC converter. From this result, the 3rd order harmonic of the input current of the AC/DC converter controlled by the PI controller with the power balance control technique is lower than that of the AC/DC converter controlled by
Fig. 12. The harmonics spectrum of the input current in Fig. 11.

Fig. 13. Simulated transient response of the CUK AC/DC converter controlled by the PI controller with the power balance control technique using the proposed sampling method.

Fig. 14. Simulated steady state input current and voltage of the converter controlled by the proposed control method.

Fig. 15. The harmonics spectrum of the input current in Fig. 14.

The conventional PI controller. Moreover, by using the power balance control technique, the magnitude of the harmonics current at a higher frequency is more than that of the AC/DC converter controlled by the PI controller. However, by using both of the voltage control techniques, the occurrence of the harmonics current is still lower than the limit defined in the harmonic regulations. To reduce the distortion of the input current of the AC/DC converter, the proposed sampling method, which senses the average output voltage, is used and the voltage loop is controlled by the PI controller with the power balance control technique for a fast transient response. The simulated transient response and the steady state response of the AC/DC converter controlled by the proposed method are shown in Fig. 13 and Fig. 14. The responses with load changes from 200 W to 100 W and from 100 W to 200 W are shown in Fig. 13. The response time is as fast as the response time, from Fig. 7, using the PI controller with the power balance control technique and the conventional sampling method. In Fig. 14 the line current has a lower distortion than the input current shown in Fig. 8 and Fig. 11. The harmonics spectrum, as shown in Fig. 15, also confirms that the converter controlled by the proposed method has a lower input current distortion than the converter using the conventional PI controller and the PI controller with the power balance control technique using the conventional sampling method.

VI. EXPERIMENTAL RESULTS

To confirm the performance of the proposed system an experiment was set up and the results were compared with those of the simulation. The parameters of the CUK AC/DC converter used in the experiment are shown in Table I. Three control strategies were implemented on a dsPIC30F4011 to control the AC/DC converter for performance comparison purposes. The implemented control methods in the experiment are the conventional PI controller, the PI controller with the power balance control technique and the PI controller with the power balance control using average output voltage measurement. The PI controller parameters of these control methods were designed so that the phase margin of the compensated system was 70° with a 15 Hz crossover frequency. The proportional gain and the integral gain of the controller are 3.86 and 132.73, respectively. For every control algorithms in the experiment, the sampling time period was set at 200µs. However, for the
proposed control algorithm, the output voltage value in the voltage control loop is updated at the zero crossing point of the ac line voltage.

A. Transient Response

The transient responses of the CUK AC/DC converter controlled by the conventional PI controller are shown in Fig. 16 and Fig. 17. The output voltage, the load current and the input inductor current of the CUK AC/DC converter at a step load from 100 W to 200 W are shown in Fig. 16. The response to a step load from 200 W to 100 W is shown in Fig. 17. From these results, it can be seen that the settling time is about 100 ms. The overshoot and the undershoot responses are approximately 0.8 V as can be seen from the responses in Fig. 18. The output voltage waveform in Fig. 18 was measured by using the ac coupling mode of an oscilloscope.

Fig. 19 shows the step response of the CUK AC/DC converter controlled by the PI controller with the power balance control technique using the conventional sampling method. The load condition in Fig. 19 was changed from 100 W to 200 W and then back to 100 W. From this result, the AC/DC converter controlled by the PI controller with the power balance control technique has a faster response in voltage control than the one using the conventional PI controller.

As a result of the proposed control method shown in Fig. 1, the sampling time period of the voltage loop is still the same as that of the two previous control algorithms. However, in the proposed method, the average output voltage is used as a feedback signal in the voltage control loop and it is updated by sampling the output voltage at the zero crossing point of the line voltage or the rectified line voltage.

From Fig. 20, the output voltage sensed at the zero crossing point of the rectified line voltage is approximate to the average output voltage. The transient responses of the AC/DC converter controlled by the proposed method are shown in Fig. 21 and Fig. 22. These results show that the proposed control method has as fast a response in voltage control as that of the converter controlled by the PI controller with the power balance control technique. The output voltage control response is very fast and the amplitude of the input inductor current is suddenly changed when the load power is step from 100 W to 200 W.

B. Steady State Response

In this section, an experiment was carried out to verify the steady state performance of the proposed control method and to compare it with those of the conventional PI controller and the PI controller with power balance control technique. Fig. 23 and Fig. 24 show the steady state response of the AC/DC converter with a 100 W load controlled by the conventional PI controller. From Fig. 23, the input current of the converter is distorted and the total harmonic distortion (THD) of the current is 10.6%, as can be seen in Fig. 24. By using the PI controller with the power balance control technique to control the converter, the steady state results with a 100 W load power are shown in Fig. 25 and Fig. 26. The input current is also distorted and its THD is approximately equal to the THD shown in Fig. 24. For Fig. 27 and Fig. 28 the load condition for the experiment is the same as the conditions for the experiments with the two previous control methods. The steady state input current of the converter controlled by the proposed control method is shown in Fig. 27. The load condition in these results is 100 W and it is the same value as in the experiments using the two previous control methods. The input current of the converter in Fig. 27 has a lower distortion than the current shown in Fig. 23 and Fig. 25. The THD of the input converter current with a 100 W load by using the proposed control method is 5.2%. The results, from Fig. 23-Fig. 26, show that the AC/DC CUK converter controlled by the proposed control method has a lower input current distortion than the two previous methods. From the results in Fig. 23-Fig. 26, it can be seen that the input current distortion
of the converter controlled by the conventional PI controller is as same as that of the CUK AC/DC converter controlled by the PI controller with the power control technique using the conventional sampling method. Using the proposed control scheme, the input current of the AC/DC converter and the harmonics content are shown in Fig. 27 and Fig. 28. These results show that the AC/DC converter controlled by the proposed method has a lower input current distortion than the two previous control methods. In general, the THD of the input current of the PFC circuit is dependent on the load power. However, with the proposed method, the average output voltage is sampled and then the influence of the voltage ripple on the voltage control loop of the PFC circuit is minimized. As a result, the THD of the input current is low even when the load is changed. The input current harmonics of the AC/DC converter controlled by the proposed method at load values of 55 W, 150 W and 200 W are shown in Fig. 29, Fig. 30 and Fig. 31, respectively.

VII. Conclusion

The control design of a CUK AC/DC converter has been presented. The digital controller was implemented on a dsPIC30F4011 DSP. The nearly average output voltage sampling method has been proposed. By the proposed method, the influence of the output voltage ripple on the control loop of the AC/DC converter is minimized. To investigate the proposed method, simulation and experimental results were obtained. The transient and the steady state performance of the CUK AC/DC converter controlled by the proposed method was compared to the CUK AC/DC converter controlled by a conventional PI controller and a PI controller with the power balance control technique using the conventional sampling method. From both the simulation and the experimental results, it can be seen that the proposed method has achieved a fast response in output voltage control and a low THD value of the input current even when the load is varied. Without using an input line filter circuit, the THD of the input current at loads of 55 W, 100 W, 150 W and 200 W are 6.5%, 5.2%, 5.2% and 5.7%, respectively.

ACKNOWLEDGMENT

This work was supported by the Graduate College and the Faculty of Engineering, King Mongkut’s University of Technology North Bangkok (KMUTNB), Bangkok, Thailand.
Fig. 23. The steady state input current and input voltage of the AC/DC converter controlled by the PI controller.

Fig. 24. The harmonics content of the input current in Fig. 23.

Fig. 25. The input current and input voltage of the AC/DC converter controlled by the PI controller with the power balance control technique.

Fig. 26. The harmonics content of the input current in Fig. 25.

Fig. 27. The steady state of the input current and the input voltage of the AC/DC converter controlled by the proposed method.

Fig. 28. The input current harmonics of the AC/DC converter with 100 W load controlled by the proposed method.
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