FPGA-based Centralized Controller for Multiple PV Generators Tied to the DC Bus

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Abstract

The integration of photovoltaic (PV) energy sources into DC grid has gained considerable attention because of its enhanced conversion efficiency with reduced number of power conversion stages. During the integration process, a local control unit is normally included with every power conversion stage of the PV source to accomplish the process of maximum power point tracking. A centralized monitoring and supervisory control unit is required for monitoring, power management, and protection of the entire system. Therefore, we propose a field-programmable gate array (FPGA) based centralized control unit that integrates all local controllers with the centralized monitoring unit. The main focus of this study is on the process of integrating many local control units into a single central unit. In this paper, we present design and optimization procedures for the hardware implementation of FPGA architecture. Furthermore, we propose a transient analysis and control design methodology with consideration of the nonlinear characteristics of the PV source. Hardware experiment results verify the efficiency of the central control unit and controller design.

Key words: Centralized controller, DC grid, FPGA, Parallel operation

I. INTRODUCTION

The rapid increase in the demand for clean energy sources has given rise to the idea of connecting PV generation sources to utility grids [1]. Worldwide PV installation exceeded 100 GW in 2012 and has a potential to increase 2.5-fold within the next five years [2]. Therefore, PV will become a major electricity source in the near future. Considerable attention has recently been directed toward the integration of PV generators with the DC grid and intermeshing such generators with the AC network, especially at low- and medium-voltage levels [3]-[5]. Integrating PV systems with the DC grid will reduce the number of power conversion stages and thus increase overall conversion efficiency. The efficiency of integrated DC loads, such as electric vehicle batteries, to the grid will also be improved [6], [7]. Different methods can be used to integrate PV sources with the DC grid. Parallel connection of PV generators is a widely used method. A DC/DC converter is used to integrate each PV generator to the DC grid.

One technological challenge arising from the integration of PV systems with multigenerator systems is overall system control. A two-layered control architecture (hierarchies) is typically required for the parallel-connected power interfaces of sources. The first layer is the local control unit, which should be included in every DC/DC converter for maximum power point tracking (MPPT) and fault protection. The second layer is the central monitoring and supervisory unit embedded in the system to monitor the fault and/or power flow of parallel-connected PV systems. Having a central monitoring unit is essential for this system, especially for typical stand-alone mode operation [8], [9]. This unit should be capable of communicating and supervising the local control units through cutting-edge communication technologies [7].

A simplified schematic diagram of the conventional control architecture is shown in Fig. 1. Several DC/DC converters, such as DC/DC conv. (1) and DC/DC conv. (n),
are connected in parallel. These converters interface between PV generators [PV G (1) to PV G (n)] with the DC bus. Every DC/DC converter in the architecture has its own local control unit [LCU (1) to LCU (n)]. The control architecture in Fig. 1 needs a local controller for each PV subsystem. Consequently, system control coordination among the modules and the cost competitiveness of the entire system worsen. Integrating local control units with the central monitoring unit will significantly reduce manufacturing cost for controller implementation because of easy coordination. Several instantaneous control tasks under stand-alone operations and islanding detection will be easier to apply. Furthermore, system installation will become easier and cheaper. System control is easily configured and updated during the system lifetime. Additionally, efficient data exchange is achieved between every local control unit and the central monitoring unit. This exchange enables the supervisory controller unit to coordinate the operation of every local controller efficiently. Therefore, integrating multiple control units into a single centralized unit provides several logical and physical proficiencies. A schematic diagram of the proposed central control and monitoring unit is shown in Fig. 2. A serious technical challenge is finding a suitable hardware device to process multiple control loops instantaneously.

In addition to analog controllers, digital signal processors (DSPs) and field-programmable gate arrays (FPGAs) are the main technological solutions for controller implementation. DSP-based controllers are more suitable for the application that process large data and complex mathematical calculations. Whereas, FPGA-based controllers are appropriate for algorithms that require parallel operation [10]. With the capability of parallel operation and fast performance, several local controllers can be integrated into a single FPGA device with equivalent or better control performance. DSP-based devices are expensive because DSPs are founded on sequential processing units. Therefore, we used an FPGA-based central control unit in this study.

This study focuses on integrating many local control units into a single central unit. The FPGA-based central control unit, including the algorithm description and FPGA design, is proposed in Section II. System modeling, transient analysis, and local controller design methodology are presented in Section III. The proposed control architecture is verified on the basis of the experimental test results in Section IV.

II. FPGA-BASED CENTRAL CONTROLLER

FPGAs consist of a matrix of configurable logic blocks (CLBs). CLBs are connected to one another through a reprogrammable network. The code downloaded to the memory cells configures both the CLBs and the network connection. The memory cells (e.g., Flash, EPROM, or SRAM) enable the FPGA to be as reprogrammable as microprocessors [11]-[13]. FPGAs consist of CLBs; each CLB unit contains a number of logic units called slices. A slice contains a number of look-up tables (LUTs) and registers. The design objective of FPGA-based controllers is to develop an optimized hardware architecture for the given algorithm. This architecture should satisfy the minimum number of slices, LUTs, and registers without violating the hardware processing time response. RAM blocks can also be used if necessary. In the first stage of the design process, the central control unit algorithm is divided into two units (modules): the MPPT algorithm and the PI controller. A data flow graph (DFG) is then used to design and optimize all units individually.

The proposed central control unit controls multiple PV systems. Each PV system contains a solar array and DC/DC boost converter, as shown in Fig. 2. The primary task of the proposed central controller is to perform MPPT control for
the individual PV subsystems tied to the common DC grid. The FPGA-based central control unit senses and utilizes the voltage and current information of each PV subsystem to perform MPPT control. The main control task of the central control and monitoring unit is to perform MPPT for every paralleled PV system. Some individual methods, such as open-circuit voltage control and short-circuit current control, as well as hill climbing, pilot cells, and artificial intelligence techniques, are applied to achieve MPPT control in PV power generation systems [14]-[19]. Hill climbing is generally used in PV applications. This approach is mainly divided into perturb and observe (P&O) and incremental conductance (IC) algorithms [20]-[23]. The simple structure, ease of implementation, and reduced calculations make the P&O algorithm suitable for FPGA implementation. The perturbation variable determines whether the P&O method is a direct or indirect control algorithm. The duty cycle for the direct controller is generally used as the perturbation variable. This method is the simplest approach to achieve MPPT. However, one vital drawback of this method is the loss of MPPT control during load voltage fluctuations. Furthermore, the uncontrolled step response in the duty cycle produces oscillations in PV output power. These oscillations misguide the controller and cause it to perturb the power away from the maximum power point, which results in low MPPT efficiency. These drawbacks can be overcome by using the feedback controller. Whereas, in the indirect control method, PV voltage or current feedback is used to track the maximum power point reference determined by the P&O algorithm. Therefore, the P&O algorithm with feedback voltage control loop is proposed in this study.

The FPGA design process for centralized MPPT control is described in Fig. 3. The first step is “algorithm refinement,” the next step is “modularity,” which consists of “hierarchy” and “regularity.” The final step is “A’ methodology,” which is a configuration optimization. The controller is configured as a prototype for the proposed scheme, as shown in Fig. 4. Every PV system has an MPPT algorithm unit and PI controller. However, FPGA slice utilization increases in this case. The individual MPPT loop for every PV subsystem is a redundant structure because of the relatively slow sampling rate required for MPPT control. Multiple MPPT controllers increase the number of slices but reduce the number that can be implemented on an FPGA chip. The optimized proposed architecture customizes a single unified MPPT controller for all PV subsystems, with every PV subsystem having its own individual PI controller. The DFG for the unified MPPT algorithm unit is shown in Fig. 5. The centralized MPPT controller collects sensor data in each PV subsystem. For example, the sensed variables of “n” PV subsystems are $V_{p1}$, $I_{p1}$, $V_{p2}$, $I_{p2}$, ..., $V_{pn}$, $I_{pn}$. The sensed data are sampled sequentially according to the required MPPT algorithm period ($T_{MPPT}$). The sampling frequency for the MPPT clock is accordingly calculated from $f_{MPPT} (= \frac{1}{T_{MPPT}})$. The reference voltages ($V_{ref}$ to $V_{ref}$) are sequentially processed in the specific PI control unit after calculation. Each reference voltage value is fixed during the specified MPPT period ($T_{MPPT}$). The multiplexer and de-multiplexer shown in Fig. 5 enable MPPT module sequence selection in a single controller along with the instantaneous parallel operation of the PI controller [23].

The device utilization for two different architectures in Fig. 4 and Fig. 5 are compared in Table I. Data are based on central control unit implementation for three PV subsystems. The used FPGA is from the Xilinx Virtex II series and has 5120 slices. By implementing the architecture in Fig. 4, we observed that slice utilization is 47.6%, excluding the PI parts. Slice utilization for a single MPPT architecture is 29.2% in Fig. 5. Implementing the three PI controllers resulted in a total slice utilization of 99% for the architecture in Fig. 4. The total slice utilization for the proposed architecture is only just 70%.

![Fig. 3. FPGA Design Methodology for centralized MPPT controller.](image)

![Fig. 4. Centralized controller with multiple MPPT algorithms for multiple PV modules.](image)
The new operating point becomes “b” and the MPPT controller should drive the PV system to maximum power point “c.” Therefore, the PI controller should track the reference under all three operating conditions. However, the nonlinear characteristics of PV systems, which is more than double the number of PV subsystems integrated in the other architecture. A design methodology for the PI feedback control loop is proposed in the next section, taking into account the nonlinear characteristics of PV sources.

III. TRANSIENT ANALYSIS AND CONTROLLER DESIGN METHODOLOGY

The PV generator is a nonlinear source of energy; the output characteristic of the PV generator is a function of solar radiation, temperature, and load conditions [24], [25]. The proposed MPPT controller is based on the P&O method. A PI controller tracks the reference voltage, which is perturbed to observe output power change and to track the maximum power point of the PV generator. The P–V curves for two different operating conditions of solar radiation are shown in Fig. 6. The PV generator tracks the maximum power point at 400 W/m² solar radiation (point “a” on the curve). A step change increases solar radiation to 1000 W/m² at this time. The new operating point becomes “b” and the MPPT controller should drive the PV system to maximum power point “c.” Therefore, the PI controller should track the reference under all three operating conditions. However, the dynamic characteristics of the PV system are dependent to the ambient operating conditions and the operating point on the P–V curve. Therefore, a PI controller designed for a single maximum power point does not guarantee successful reference tracking under other operating conditions. A case demonstrating this problem is shown by the experimental results shown in Fig. 7. The PI controller fails to track the reference at low solar radiation (between a and b). However, the PI controller was able to track the MPP at high radiation condition.

A PV generator model is derived with solar radiation and temperature as model input parameters. Subsequently, a transient analysis is applied to provide a generalized transfer function for the proposed PV system. According to the analysis, a Bode plot is sketched for different operating conditions to address the worst-case operating points.

The Shockley diode equivalent circuit model shown in Fig. 8 is used to model the PV module or array of modules. Current source $I_{light}$ simulates the photo-generated current. $I_{diode}$ represents the diode current of the solar cell PN junction. Shunt and series resistances represent the losses in the
semiconductor body and across the wire connections, respectively [26]-[32]. Then, by Kirchhoff’s current law,

\[
I_{PV} = I_{light} - I_{diode} - I_{sh}.
\]

From this equation, \(I_{PV}\) can be calculated by using Equation (2). To simplify the analysis, the effect of shunt resistance \((R_{sh})\) is neglected.

\[
I_{pv} = I_{light} - I_{pv} \left( \frac{V_m + I_{pv} R_s}{R_{sh}} \right)
\]

Where,

\[
A = \frac{mK T}{q}.
\]

The light generated current \((I_{light})\) near the short circuit current \((I_{sc})\) is calculated from Equation (4). The subscript “ref” in all equations specifies the reference conditions of solar radiation and temperature, which are 1000 W/m\(^2\) and 25 \(^\circ\)C, respectively.

\[
I_{light} \equiv I_{sc} \equiv \frac{S}{S_{ref}}(I_{sc,ref} + \mu_s(T_c - T_{c,ref})).
\]

According to this model, a transient analysis is provided to derive a generalized transfer function for the PV system. The DC/DC converter used is a boost converter that integrates the PV source with the DC link. The equivalent circuit of the PV system is shown in Fig. 9. A voltage sink models the DC link, including DC link disturbance. Solar radiation \((S)\), cell temperature \((T_c)\), DC link voltage \((e)\), and DC/DC boost converter duty ratio \((d)\) are the equivalent circuit model inputs. The dynamic system model outputs are PV generator output voltage \(V_{pv}\) and output current \(i_{pv}\). The temperature dynamic is ignored for the small signal model. Assuming an ideal boost converter, Equations (5) and (6) are derived.

\[
v_{pv} = \frac{1}{C_{in}}(i_{pv} - i_L)
\]

\[
i_L = \frac{1}{L}(v_{pv} - e(1-d))
\]

After linearizing equations (5) and (6), each PV subsystem state space parameter can be represented by equations (7) and (8). Prefix \(\delta\) denotes the small signal increments, whereas subscript \(0\) indicates steady-state values. Constants \(K_s\) and \(K_{PV}\) are defined in equations (9) and (10). \(R_{PV}\) is the instantaneous dynamic resistance defined in Equation (11).

Finally, transfer function \(GV_{pv}(s)\) from \(\delta d(s)\) to \(\delta V_{pv}(s)\) can be derived from the state space representation. This transfer function is for a specific solar radiation and cell temperature condition. The temperature value affects \(K_{PV}\) and transient characteristics. Solar radiation affects the transfer function dynamics and the steady-state values \(V_{PV0}\) and \(I_{PV0}\).

\[
GV_{pv}(s) = \frac{\delta V_{pv}(s)}{\delta d(s)} = \frac{-E_0}{LC_{in}s^2 - L/R_{PV}s + 1}
\]

Equation (12) indicates that the transfer function is of the second order and is a function of dynamic transient resistance \(R_{PV}\), which is a function of solar radiation, temperature, and load operating point. The I–V curves at two different solar radiation conditions are shown in Fig. 10. \(R_{PV}\) versus \(V_{PV}\) under the same conditions of the I–V curve is shown in Fig. 10(b). The maximum \(R_{PV}\) occurs during transient response (point “b”) because \(V_{PV}\) is the minimum and \(i_{PV}\) is the maximum among the three points. Bode plots for \(GV_{PV}\) are sketched in Fig. 11 for different operating points in the I–V curve of Fig. 10. Bode plots show that point “c” has a peak Q-factor, implying an oscillatory system. The system becomes more damped as \(V_{PV}\) increases to point “c.” The system goes through all operating conditions under a typical PV system operation (see Figs. 6 and 7).
Fig. 10. V–I characteristics according to radiation change: (a) V–I characteristics and (b) R–V characteristics.

Fig. 11. Bode Plot for the PV system.

According to the analysis and discussion, the system should be designed for worst-case conditions to ensure stability and robust controller performance. The worst-case design point, including PV system transient trajectories, is the lowest operating voltage from the Bode plots in Fig. 11. This value should be carefully derived from the V–I curves under different solar radiation and temperature conditions.

IV. EXPERIMENTAL VERIFICATION

The experimental test aims to verify the capability of the FPGA-based centralized controller to perform MPPT for multiple PV systems connected to a DC grid. A centralized control unit with seven PI control loops was developed; this unit can control seven PV systems. The control unit was embedded in the National Instrument FPGA PXI 7831R unit, which contains a Xilinx Virtex VII00 FPGA chip. A dual PV TerraSAS simulator was used to emulate two different PV arrays. During the test, all seven loops were tied to each array and made to work continuously in two groups to demonstrate the parallel operation capability of the FPGA device. The switching frequency was 30 kHz, and the sampling time of the PI feedback control loop was 33 μs. If performance is to be realized by using DSP instead of FPGA, then a high-performance and expensive device is required to satisfy the speed requirement of multiple loop numbers.

A schematic diagram of the experimental setup is shown in Fig. 12. A test rig image is shown in Fig. 13. Each channel of the solar simulator emulates the individual solar array with a different V–I curve, as shown in Fig. 14. Channels 1 and 2 are rated as 65W and 80W, respectively. The different PV characteristics of the two channels show that both MPPT controllers work independently. The cloudy day profile shown in Fig. 15 emulates solar radiation and ambient temperature conditions for both simulator channels.
The cloudy day profile was applied to both simulator channels for 150 min real time. A boost converter connected each simulator channel to the unified DC-link, which was emulated by a PRO DIGITAL 3314 DC electronic load (Fig. 13).

The central controller operation was verified by the test results presented in Fig. 16. The proposed central control unit swiftly and accurately tracks the maximum power points, even under fast-changing conditions of solar radiation and temperature for both channels. When the solar radiation and temperature profile of the solar array simulator was set to slowly changing environmental condition at the solar radiation of 400W/m² at 700s, average MPPT efficiency of 99% was obtained. However, when the radiation and temperature profiles were rapidly changed at the solar radiation of 400W/m² at 1000s, MPPT efficiency of 95% was measured. Both PV sources always exhibited different $V_{PV}$ and $P_{PV}$, which indicates that all control units operate independent of one another. MPPT efficiency versus $V_{PV}$ is shown in Fig. 17 using the data obtained from the experimental results for both channels in Fig. 16. The figure demonstrates an average efficiency (slightly more than 97%) even under harsh operating conditions for both channels. The average efficiencies are calculated using MATLAB software.

The results demonstrate that many PI control loops with high sampling frequencies could be unified into a single FPGA chip without affecting control loop performance. Moreover, the unified MPPT controller works successfully even under cloudy weather conditions because of the robustness of the FPGA control design methodology.
V. CONCLUSIONS

We presented a new FPGA-based central control unit for multiple PV systems connected to a DC grid. This study considered the parallelism of FPGA, which makes FPGAs different from DSPs. We proposed a detailed design process for implementing the centralized controller. This process includes the optimization of FPGA logic utilization. MPPT control for each PV system is the basic operation of the proposed centralized control unit, in which the PV system also requires an individual feedback-control inner loop.

We considered the worst-case $V$–$I$ characteristic curve of the PV system in the proposed design methodology for the implementation of feedback-control loop. The PV system transfer function changes from a damping region to an oscillatory region when PV voltage changes under the transient response of the power conditioner.

Finally, we implemented and verified the central controller on the basis of experimental results. We used a dual channel simulator to simulate two PV systems. However, seven PI control loops were implemented on a single FPGA during the test. Each PI control loop is designed to operate at a sampling frequency of 30 kHz. The results show that the FPGA central controller can control the two PV groups in parallel. MPPT efficiencies obtained for both channels were slightly more than 97%.

In future work, a battery bank will be included in the DC grid, whereas a battery charger controller will be included in the central control unit. Furthermore, a supervisory controller will be introduced to manage the different modes of system operation.

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